

4th International Seminar on New Issues in Artificial Intelligence

Madrid, Jan. 31st - Feb. 4th 2011

EFFICIENT BOOLEAN REASONING: SAT, PREFERENCES & QBFs

PROPOSITIONAL SATISFIABILITY (SAT)

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Motivations

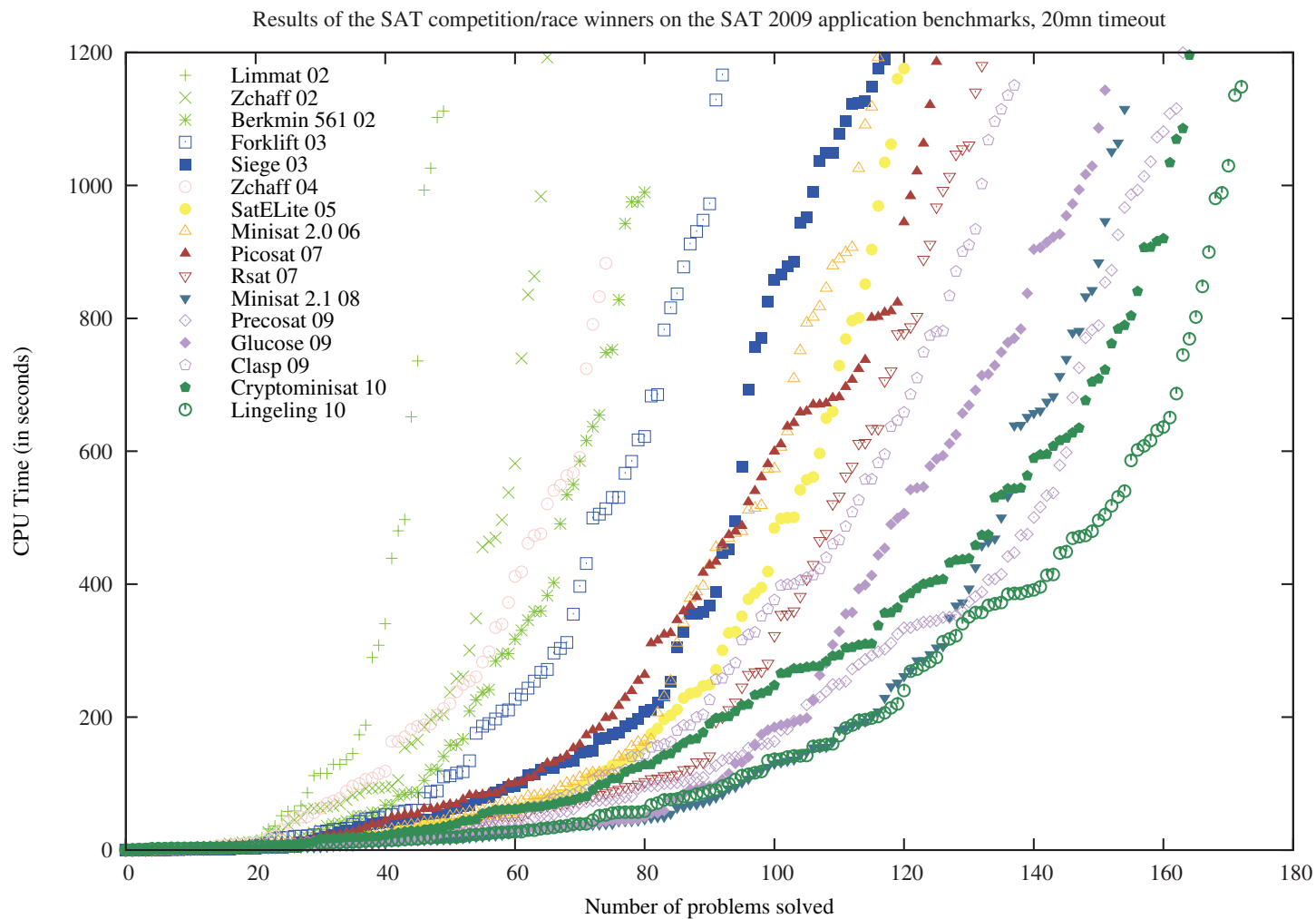
- ▷ Last ten years: **impressive advance in Boolean reasoning techniques**
 - extremely efficient solvers [52, 46, 4, 29, 36, 55, 23]
 - hard “real-world” problems encoded into SAT (e.g.,
 - planning
 - model checking
 - circuit and software testing
 - security & criptanalysis
 - reasoning on conceptual models
 - bioinformatics
 - feature extraction from images
 - ...

Motivations

Application benchmarks submitted to the last SAT competition (2009):

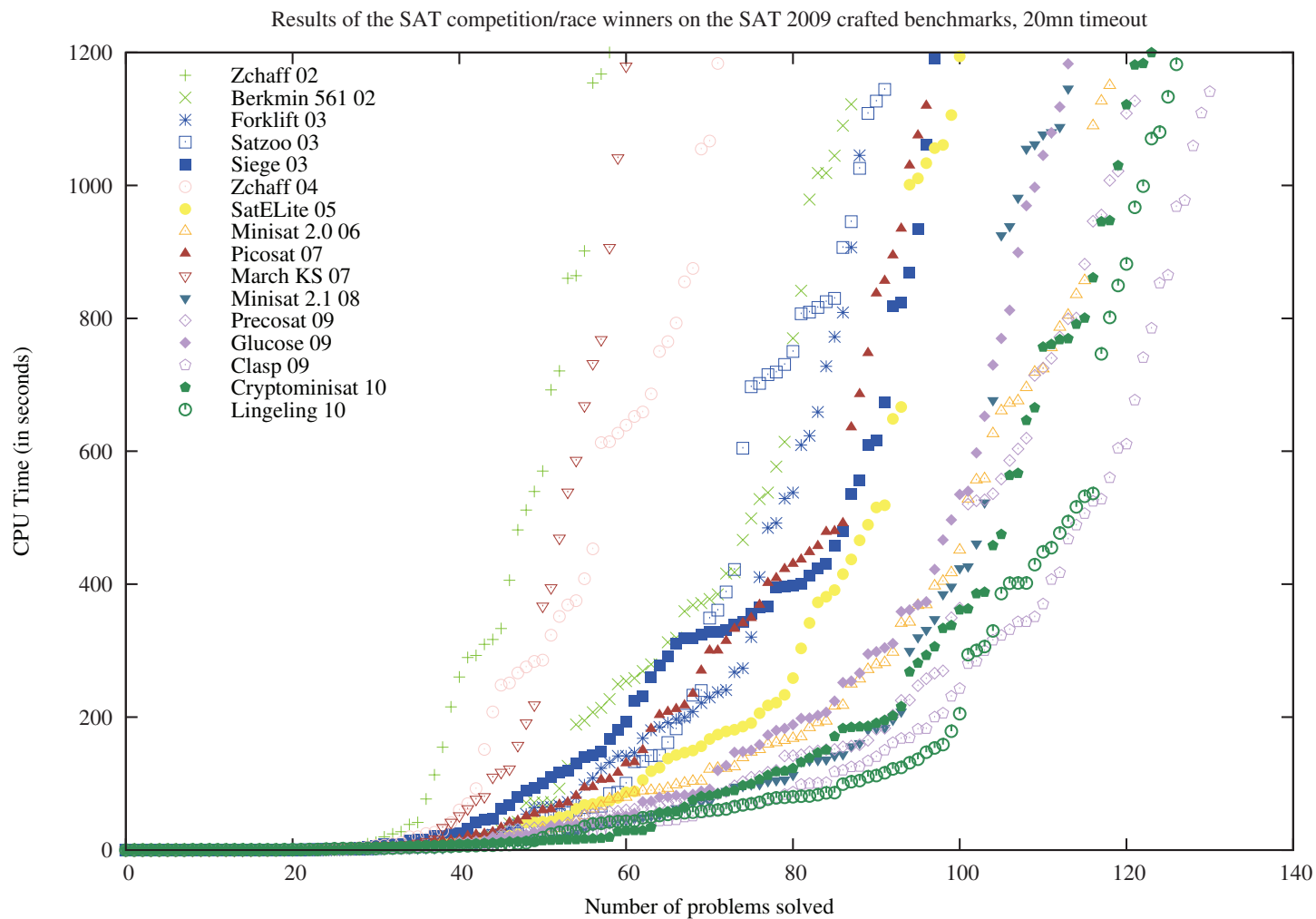
1. Aprove: Term Rewriting systems benchmarks.
2. BioInfo I: Queries to find the maximal size of a biological behavior without cycles in discrete genetic networks.
3. BioInfo II: Evolutionary trees.
4. Bit Verif: Bit precise software verification generated by the SMT solver Boolector.
5. C32SAT: Software verification generated by the C32SAT satisfiability checker for C programs.
6. Crypto: Encode attacks for both the DES and MD5 crypto systems.
7. Diagnosis: 4 different encodings of discrete event systems.

Motivations



Courtesy by Daniel Le Berre

Motivations



Courtesy by Daniel Le Berre

Content

- ⇒ Basics on SAT
- NNF, CNF and conversions
- Basic SAT techniques
- Modern SAT Solvers
- Advanced Functionalities: proofs, unsat cores, interpolants

Basic notation & definitions

▷ Boolean formula

- \top, \perp are formulas
- A **propositional atom** A_1, A_2, A_3, \dots is a formula;
- if φ_1 and φ_2 are formulas, then $\neg\varphi_1, \varphi_1 \wedge \varphi_2, \varphi_1 \vee \varphi_2, \varphi_1 \rightarrow \varphi_2, \varphi_1 \leftrightarrow \varphi_2$ are formulas.

▷ **Literal**: a propositional atom A_i (positive literal) or its negation $\neg A_i$ (negative literal)

▷ N.B.: if $l := \neg A_i$, then $\neg l := A_i$

▷ **Atoms**(φ): the set $\{A_1, \dots, A_N\}$ of atoms occurring in φ .

▷ a Boolean formula can be represented as a **tree** or as a **DAG**

TREE and DAG representation of formulas: example

$$(A_1 \leftrightarrow A_2) \leftrightarrow (A_3 \leftrightarrow A_4)$$

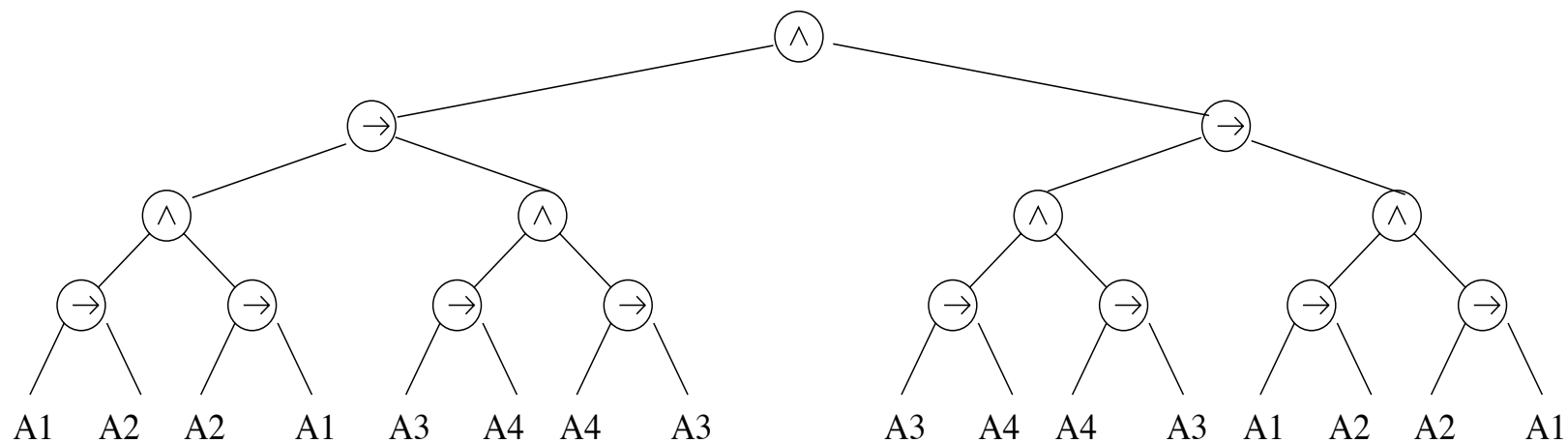
$$\Downarrow$$

$$\begin{aligned} &(((A_1 \leftrightarrow A_2) \rightarrow (A_3 \leftrightarrow A_4)) \wedge \\ &((A_3 \leftrightarrow A_4) \rightarrow (A_1 \leftrightarrow A_2))) \end{aligned}$$

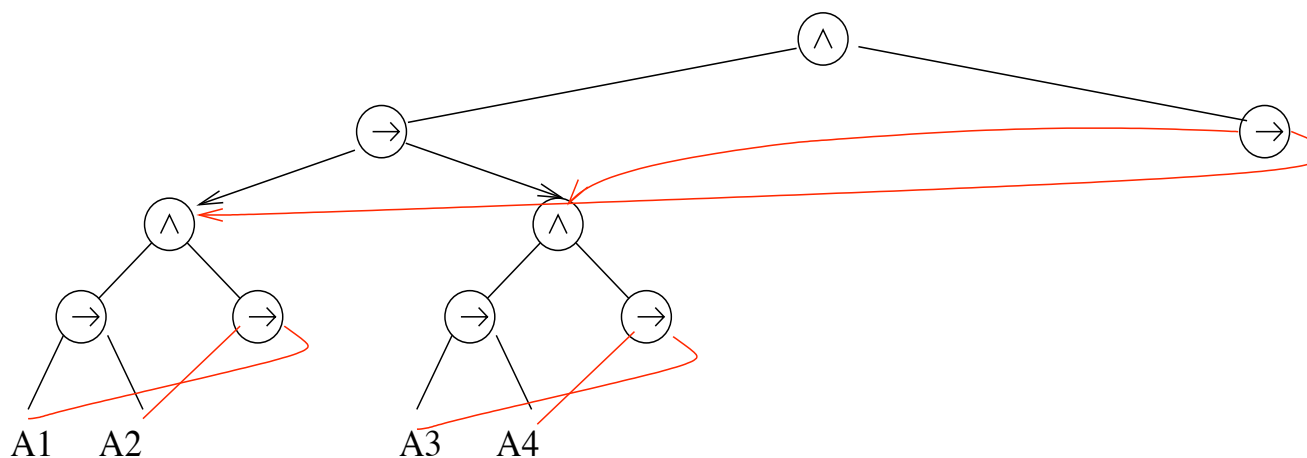
$$\Downarrow$$

$$\begin{aligned} &(((A_1 \rightarrow A_2) \wedge (A_2 \rightarrow A_1)) \rightarrow ((A_3 \rightarrow A_4) \wedge (A_4 \rightarrow A_3))) \wedge \\ &(((A_3 \rightarrow A_4) \wedge (A_4 \rightarrow A_3)) \rightarrow (((A_1 \rightarrow A_2) \wedge (A_2 \rightarrow A_1)))) \end{aligned}$$

TREE and DAG representation of formulas: example (cont)



Tree Representation



DAG Representation

Basic notation & definitions (cont)

▷ **Total truth assignment** μ for φ :

$$\mu : Atoms(\varphi) \mapsto \{\top, \perp\}.$$

▷ **Partial Truth assignment** μ for φ :

$$\mu : \mathcal{A} \mapsto \{\top, \perp\}, \mathcal{A} \subset Atoms(\varphi).$$

▷ **Set and formula representation of an assignment:**

• μ can be represented as a set of literals:

$$\text{EX: } \{\mu(A_1) := \top, \mu(A_2) := \perp\} \implies \{A_1, \neg A_2\}$$

• μ can be represented as a formula:

$$\text{EX: } \{\mu(A_1) := \top, \mu(A_2) := \perp\} \implies A_1 \wedge \neg A_2$$

Basic notation & definitions (cont)

φ_1	φ_2	$\neg\varphi_1$	$\varphi_1 \wedge \varphi_2$	$\varphi_1 \vee \varphi_2$	$\varphi_1 \rightarrow \varphi_2$	$\varphi_1 \leftrightarrow \varphi_2$
\perp	\perp	\top	\perp	\perp	\top	\top
\perp	\top	\top	\perp	\top	\top	\perp
\top	\perp	\perp	\perp	\top	\perp	\perp
\top	\top	\perp	\top	\top	\top	\top

N.B.:

$$\varphi_1 \vee \varphi_2 := \neg(\neg\varphi_1 \wedge \neg\varphi_2),$$

$$\varphi_1 \rightarrow \varphi_2 := (\neg\varphi_1 \vee \varphi_2),$$

$$\varphi_1 \leftrightarrow \varphi_2 := (\varphi_1 \rightarrow \varphi_2) \wedge (\varphi_2 \rightarrow \varphi_1).$$

Basic notation & definitions (cont)

▷ $\mu \models \varphi$ (μ satisfies φ):

- $\mu \models A_i \iff \mu(A_i) = \top$
- $\mu \models \neg\varphi \iff \text{not } \mu \models \varphi$
- $\mu \models \varphi_1 \wedge \varphi_2 \iff \mu \models \varphi_1 \text{ and } \mu \models \varphi_2$
- $\mu \models \varphi_1 \vee \varphi_2 \iff \mu \models \varphi_1 \text{ or } \mu \models \varphi_2$
- $\mu \models \varphi_1 \rightarrow \varphi_2 \iff \text{if } \mu \models \varphi_1, \text{ then } \mu \models \varphi_2$
- $\mu \models \varphi_1 \leftrightarrow \varphi_2 \iff \mu \models \varphi_1 \text{ iff } \mu \models \varphi_2$

▷ φ is **satisfiable** iff $\mu \models \varphi$ for some μ

▷ $\varphi_1 \models \varphi_2$ (φ_1 entails φ_2):

$\varphi_1 \models \varphi_2$ iff for every μ $\mu \models \varphi_1 \implies \mu \models \varphi_2$

▷ $\models \varphi$ (φ is valid):

$\models \varphi$ iff for every μ $\mu \models \varphi$

▷ φ is valid $\iff \neg\varphi$ is not satisfiable

Equivalence and equi-satisfiability

- ▷ φ_1 and φ_2 are **equivalent** iff, for every μ ,
 $\mu \models \varphi_1$ iff $\mu \models \varphi_2$
- ▷ φ_1 and φ_2 are **equi-satisfiable** iff
exists μ_1 s.t. $\mu_1 \models \varphi_1$ iff exists μ_2 s.t. $\mu_2 \models \varphi_2$
- ▷ φ_1, φ_2 **equivalent**
 $\Downarrow \Uparrow$
 φ_1, φ_2 **equi-satisfiable**
- ▷ **EX:** $\varphi_1 \vee \varphi_2$ and $(\varphi_1 \vee \neg A_3) \wedge (A_3 \vee \varphi_2)$ are in general **equi-satisfiable**
but **not equivalent**.

Complexity

- ▷ For N variables, there are up to 2^N truth assignments to be checked.
- ▷ The problem of deciding the **satisfiability** of a propositional formula is **NP-complete** [10].
- ▷ The most important logical problems (**validity**, **inference**, **entailment**, **equivalence**, ...) can be straightforwardly reduced to **satisfiability**, and are thus **(co)NP-complete**.



No existing worst-case-polynomial algorithm.

Content

- ✓ Basics on SAT
- ⇒ NNF, CNF and conversions
 - Basic SAT techniques
 - Modern SAT Solvers
 - Advanced Functionalities: proofs, unsat cores, interpolants

Negative normal form (NNF)

▷ φ is in **Negative normal form** iff it is given only by applications of \wedge, \vee to literals.

▷ every φ can be reduced into NNF:

1. substituting all \rightarrow 's and \leftrightarrow 's:

$$\varphi_1 \rightarrow \varphi_2 \implies \neg\varphi_1 \vee \varphi_2$$

$$\varphi_1 \leftrightarrow \varphi_2 \implies (\neg\varphi_1 \vee \varphi_2) \wedge (\varphi_1 \vee \neg\varphi_2)$$

2. pushing down negations recursively:

$$\neg(\varphi_1 \wedge \varphi_2) \implies \neg\varphi_1 \vee \neg\varphi_2$$

$$\neg(\varphi_1 \vee \varphi_2) \implies \neg\varphi_1 \wedge \neg\varphi_2$$

$$\neg\neg\varphi_1 \implies \varphi_1$$

▷ Preserves the **equivalence** of formulas.

NNF: example

$$(A_1 \leftrightarrow A_2) \leftrightarrow (A_3 \leftrightarrow A_4)$$

$$\Downarrow$$

$$\begin{aligned} & (((A_1 \rightarrow A_2) \wedge (A_1 \leftarrow A_2)) \rightarrow ((A_3 \rightarrow A_4) \wedge (A_3 \leftarrow A_4))) \wedge \\ & (((A_1 \rightarrow A_2) \wedge (A_1 \leftarrow A_2)) \leftarrow ((A_3 \rightarrow A_4) \wedge (A_3 \leftarrow A_4))) \end{aligned}$$

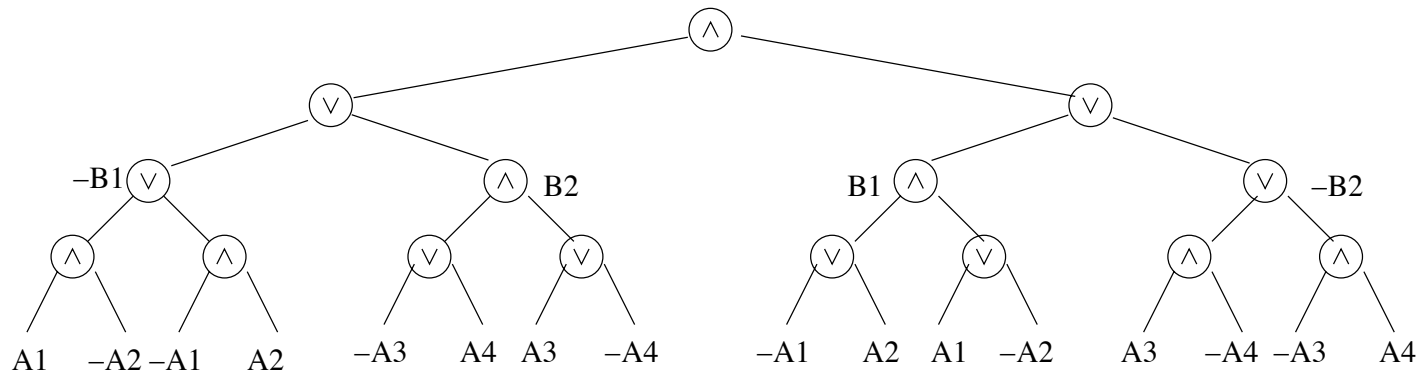
$$\Downarrow$$

$$\begin{aligned} & ((\neg((\neg A_1 \vee A_2) \wedge (A_1 \vee \neg A_2)) \vee ((\neg A_3 \vee A_4) \wedge (A_3 \vee \neg A_4))) \wedge \\ & (((\neg A_1 \vee A_2) \wedge (A_1 \vee \neg A_2)) \vee \neg((\neg A_3 \vee A_4) \wedge (A_3 \vee \neg A_4)))) \end{aligned}$$

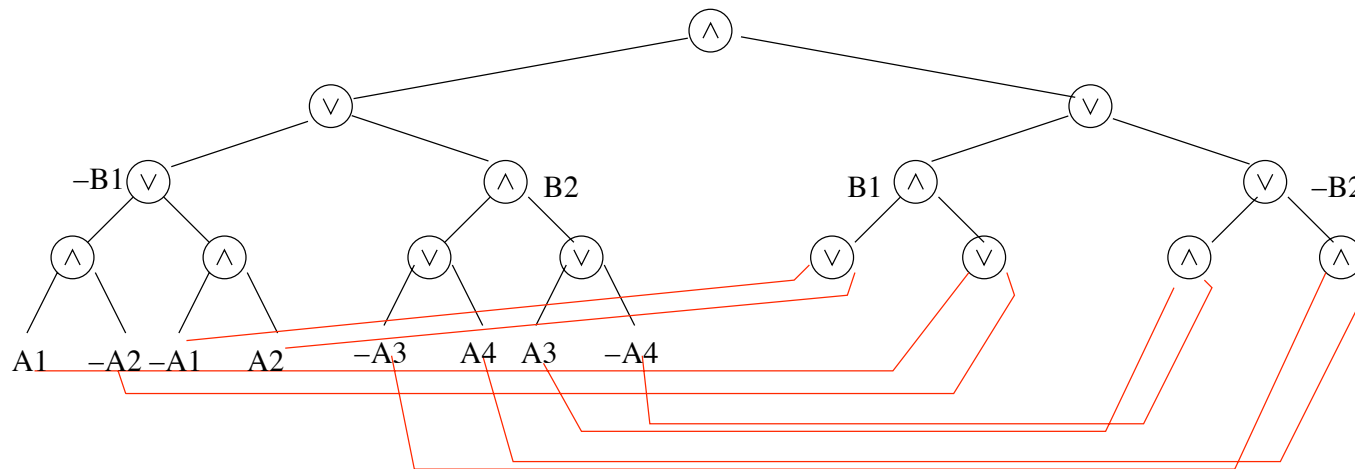
$$\Downarrow$$

$$\begin{aligned} & (((A_1 \wedge \neg A_2) \vee (\neg A_1 \wedge A_2)) \vee ((\neg A_3 \vee A_4) \wedge (A_3 \vee \neg A_4))) \wedge \\ & (((\neg A_1 \vee A_2) \wedge (A_1 \vee \neg A_2)) \vee ((A_3 \wedge \neg A_4) \vee (\neg A_3 \wedge A_4)))) \end{aligned}$$

NNF: example (cont)



Tree Representation



DAG Representation

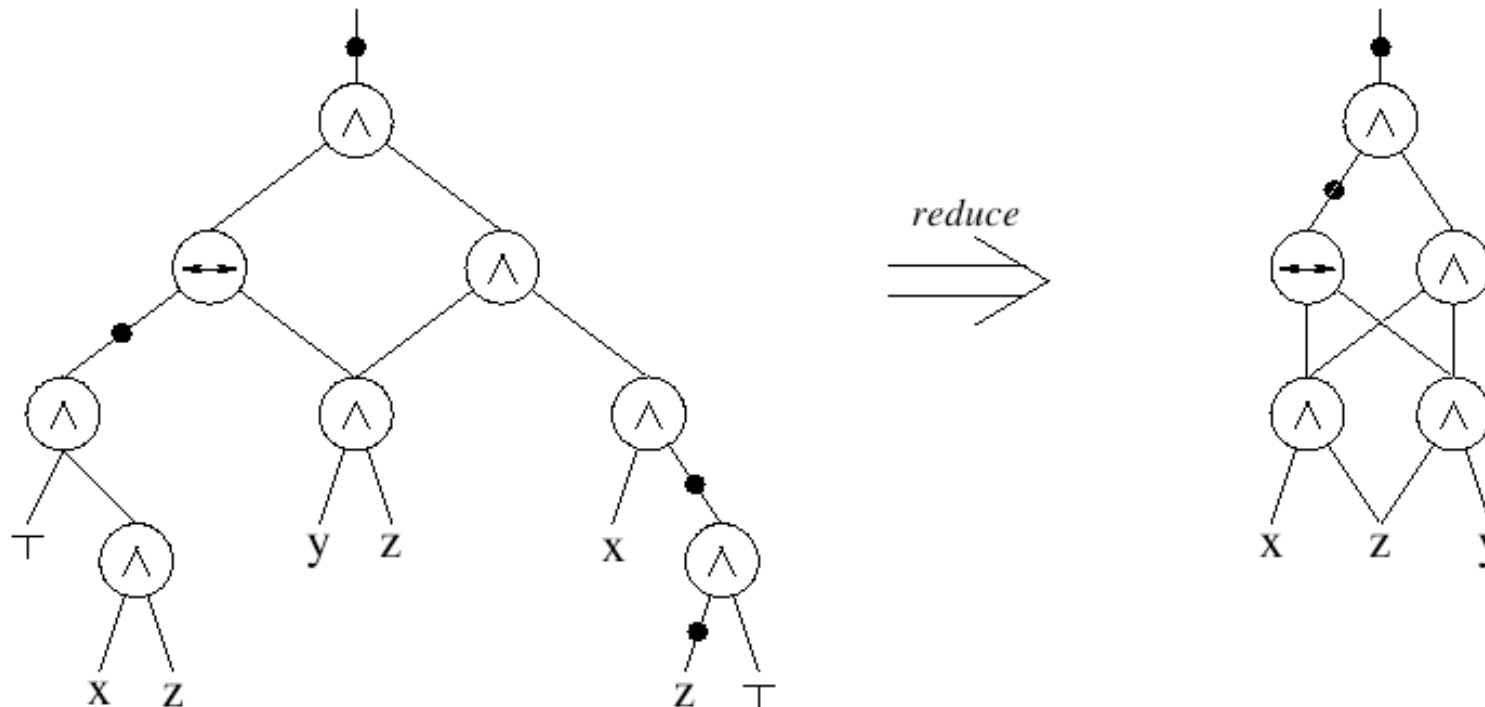
N.B. For each non-literal subformula φ , φ and $\neg\varphi$ have different representations \implies they are not shared.

Optimized polynomial representations

Reduced Boolean Circuits [1], Boolean Expression Diagrams [51].

▷ Maximize the sharing in DAG representations:

$\{\wedge, \leftrightarrow, \neg\}$ -only, negations on arcs, sorting of subformulae, lifting of \neg 's over \leftrightarrow 's,...



Conjunctive Normal Form (CNF)

- ▷ φ is in **Conjunctive normal form** iff it is a conjunction of disjunctions of literals:

$$\bigwedge_{i=1}^L \bigvee_{j_i=1}^{K_i} l_{j_i}$$

- ▷ the disjunctions of literals $\bigvee_{j_i=1}^{K_i} l_{j_i}$ are called **clauses**
- ▷ Easier to handle: list of lists of literals.
⇒ no reasoning on the recursive structure of the formula

Classic CNF Conversion $CNF(\varphi)$

- ▷ Every φ can be reduced into CNF by, e.g.,
 1. converting it into NNF;
 2. applying recursively the DeMorgan's Rule:
$$(\varphi_1 \wedge \varphi_2) \vee \varphi_3 \implies (\varphi_1 \vee \varphi_3) \wedge (\varphi_2 \vee \varphi_3)$$
- ▷ Worst-case **exponential**.
- ▷ $Atoms(CNF(\varphi)) = Atoms(\varphi)$.
- ▷ $CNF(\varphi)$ is **equivalent** to φ .
- ▷ Rarely used in practice.

Labeling CNF conversion $CNF_{label}(\varphi)$ [39, 13]

- ▷ Every φ can be reduced into CNF by, e.g., applying recursively bottom-up the rules:

$$\varphi \implies \varphi[(l_i \vee l_j)|B] \wedge CNF(B \leftrightarrow (l_i \vee l_j))$$

$$\varphi \implies \varphi[(l_i \wedge l_j)|B] \wedge CNF(B \leftrightarrow (l_i \wedge l_j))$$

$$\varphi \implies \varphi[(l_i \leftrightarrow l_j)|B] \wedge CNF(B \leftrightarrow (l_i \leftrightarrow l_j))$$

l_i, l_j being literals and B being a “new” variable.

- ▷ Worst-case **linear**.
- ▷ $Atoms(CNF_{label}(\varphi)) \supseteq Atoms(\varphi)$.
- ▷ $CNF_{label}(\varphi)$ is **equi-satisfiable** w.r.t. φ .
- ▷ Non-normal.
- ▷ More used in practice.

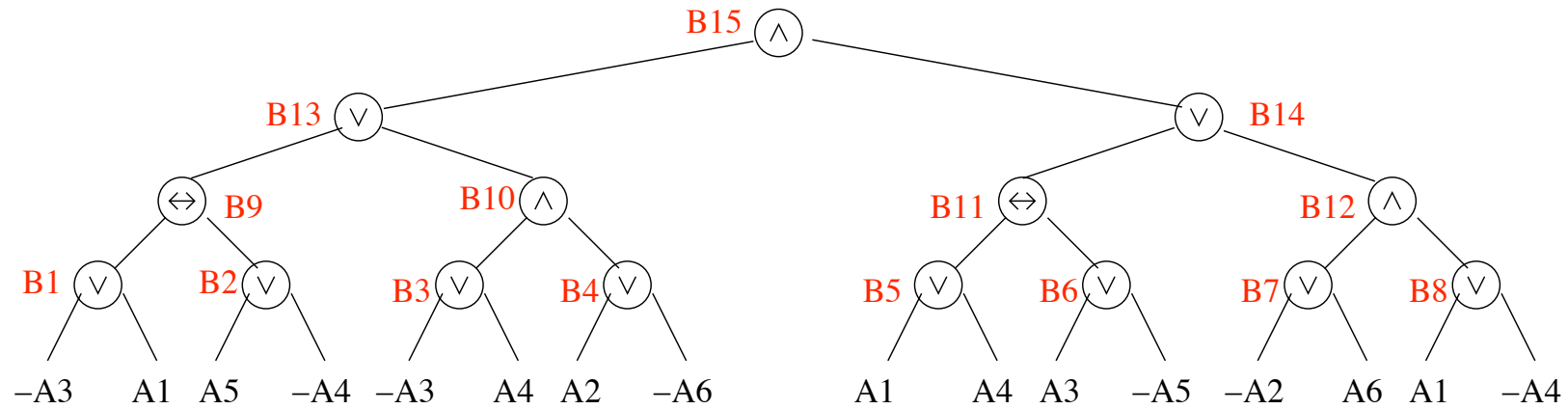
Labeling CNF conversion $CNF_{label}(\varphi)$ (cont.)

$$CNF(B \leftrightarrow (l_i \vee l_j)) \iff (\neg B \vee l_i \vee l_j) \wedge \\ (B \vee \neg l_i) \wedge \\ (B \vee \neg l_j)$$

$$CNF(B \leftrightarrow (l_i \wedge l_j)) \iff (\neg B \vee l_i) \wedge \\ (\neg B \vee l_j) \wedge \\ (B \vee \neg l_i \neg l_j)$$

$$CNF(B \leftrightarrow (l_i \leftrightarrow l_j)) \iff (\neg B \vee \neg l_i \vee l_j) \wedge \\ (\neg B \vee l_i \vee \neg l_j) \wedge \\ (B \vee l_i \vee l_j) \wedge \\ (B \vee \neg l_i \vee \neg l_j)$$

Labeling CNF conversion CNF_{label} – example



$CNF(B_1 \leftrightarrow (\neg A_3 \vee A_1)) \quad \wedge$

... \wedge

$CNF(B_8 \leftrightarrow (A_1 \vee \neg A_4)) \quad \wedge$

$CNF(B_9 \leftrightarrow (B_1 \leftrightarrow B_2)) \quad \wedge$

... \wedge

$CNF(B_{12} \leftrightarrow (B_7 \wedge B_8)) \quad \wedge$

$CNF(B_{13} \leftrightarrow (B_9 \vee B_{10})) \quad \wedge$

$CNF(B_{14} \leftrightarrow (B_{11} \vee B_{12})) \quad \wedge$

$CNF(B_{15} \leftrightarrow (B_{13} \wedge B_{14})) \quad \wedge$

B_{15}

Labeling CNF conversion CNF_{label} (improved)

▷ As in the previous case, applying instead the rules:

$$\varphi \implies \varphi[(l_i \vee l_j)|B] \wedge CNF(B \rightarrow (l_i \vee l_j)) \quad \text{if } (l_i \vee l_j) \text{ pos.}$$

$$\varphi \implies \varphi[(l_i \vee l_j)|B] \wedge CNF((l_i \vee l_j) \rightarrow B) \quad \text{if } (l_i \vee l_j) \text{ neg.}$$

$$\varphi \implies \varphi[(l_i \wedge l_j)|B] \wedge CNF(B \rightarrow (l_i \wedge l_j)) \quad \text{if } (l_i \wedge l_j) \text{ pos.}$$

$$\varphi \implies \varphi[(l_i \wedge l_j)|B] \wedge CNF((l_i \wedge l_j) \rightarrow B) \quad \text{if } (l_i \wedge l_j) \text{ neg.}$$

$$\varphi \implies \varphi[(l_i \leftrightarrow l_j)|B] \wedge CNF(B \rightarrow (l_i \leftrightarrow l_j)) \quad \text{if } (l_i \leftrightarrow l_j) \text{ pos.}$$

$$\varphi \implies \varphi[(l_i \leftrightarrow l_j)|B] \wedge CNF((l_i \leftrightarrow l_j) \rightarrow B) \quad \text{if } (l_i \leftrightarrow l_j) \text{ neg.}$$

▷ Smaller in size:

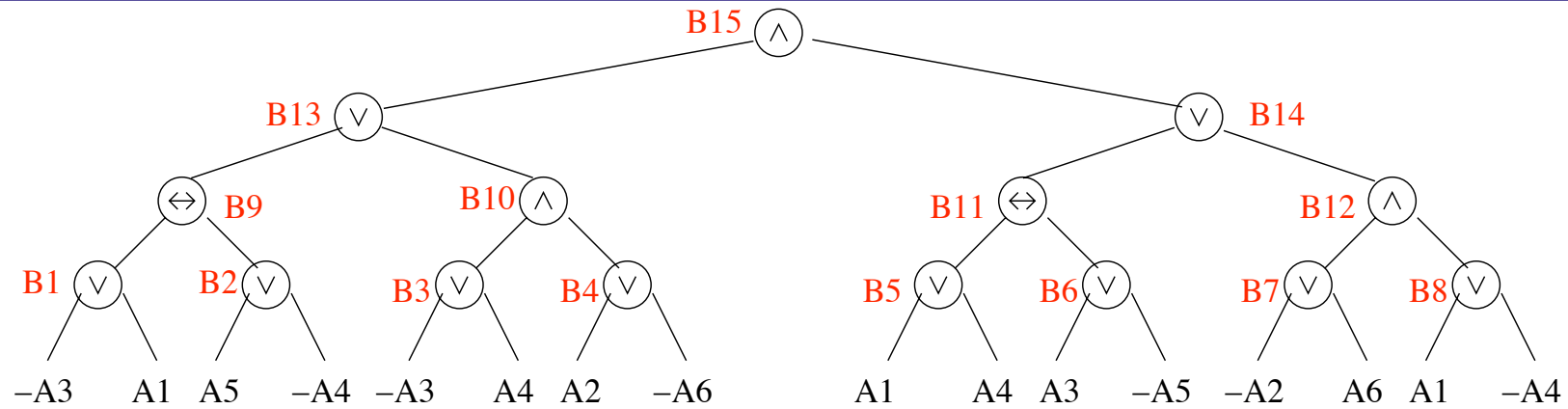
$$CNF(B \rightarrow (l_i \vee l_j)) = (\neg B \vee l_i \vee l_j)$$

$$CNF(((l_i \vee l_j) \rightarrow B)) = (\neg l_i \vee B) \wedge (\neg l_j \vee B)$$

Labeling CNF conversion $CNF_{label}(\varphi)$ (cont.)

$CNF(B \rightarrow (l_i \vee l_j))$	\iff	$(\neg B \vee l_i \vee l_j)$
$CNF(B \leftarrow (l_i \vee l_j))$	\iff	$(B \vee \neg l_i) \wedge$ $(B \vee \neg l_j)$
$CNF(B \rightarrow (l_i \wedge l_j))$	\iff	$(\neg B \vee l_i) \wedge$ $(\neg B \vee l_j)$
$CNF(B \leftarrow (l_i \wedge l_j))$	\iff	$(B \vee \neg l_i \neg l_j)$
$CNF(B \rightarrow (l_i \leftrightarrow l_j))$	\iff	$(\neg B \vee \neg l_i \vee l_j) \wedge$ $(\neg B \vee l_i \vee \neg l_j)$
$CNF(B \leftarrow (l_i \leftrightarrow l_j))$	\iff	$(B \vee l_i \vee l_j) \wedge$ $(B \vee \neg l_i \vee \neg l_j)$

Labeling CNF conversion CNF_{label} – example



Basic

Improved

$$CNF(B_1 \leftrightarrow (\neg A_3 \vee A_1)) \quad \wedge$$

$$CNF(B_1 \leftrightarrow (\neg A_3 \vee A_1)) \quad \wedge$$

$$\dots \quad \wedge$$

$$\dots \quad \wedge$$

$$CNF(B_8 \leftrightarrow (A_1 \vee \neg A_4)) \quad \wedge$$

$$CNF(B_8 \rightarrow (A_1 \vee \neg A_4)) \quad \wedge$$

$$CNF(B_9 \leftrightarrow (B_1 \leftrightarrow B_2)) \quad \wedge$$

$$CNF(B_9 \rightarrow (B_1 \leftrightarrow B_2)) \quad \wedge$$

$$\dots \quad \wedge$$

$$\dots \quad \wedge$$

$$CNF(B_{12} \leftrightarrow (B_7 \wedge B_8)) \quad \wedge$$

$$CNF(B_{12} \rightarrow (B_7 \wedge B_8)) \quad \wedge$$

$$CNF(B_{13} \leftrightarrow (B_9 \vee B_{10})) \quad \wedge$$

$$CNF(B_{13} \rightarrow (B_9 \vee B_{10})) \quad \wedge$$

$$CNF(B_{14} \leftrightarrow (B_{11} \vee B_{12})) \quad \wedge$$

$$CNF(B_{14} \rightarrow (B_{11} \vee B_{12})) \quad \wedge$$

$$CNF(B_{15} \leftrightarrow (B_{13} \wedge B_{14})) \quad \wedge$$

$$CNF(B_{15} \rightarrow (B_{13} \wedge B_{14})) \quad \wedge$$

B_{15}

B_{15}

Labeling CNF conversion CNF_{label} – further optimizations

- ▷ Do not apply CNF_{label} when not necessary:
 (e.g., $CNF_{label}(\varphi_1 \wedge \varphi_2) \implies CNF_{label}(\varphi_1) \wedge \varphi_2$,
 if φ_2 already in CNF)
- ▷ Apply Demorgan's rules where it is more effective: [13] (e.g.,
 $CNF_{label}(\varphi_1 \wedge (A \rightarrow (B \wedge C))) \implies CNF_{label}(\varphi_1) \wedge (\neg A \vee B) \wedge (\neg A \vee C)$)
- ▷ exploit the associativity of \wedge 's and \vee 's:

$$\dots \underbrace{(A_1 \vee (A_2 \vee A_3))}_{B} \dots \implies \dots CNF(B \leftrightarrow (A_1 \vee A_2 \vee A_3)) \dots$$
- ▷ before applying CNF_{label} , rewrite the initial formula so that to maximize the sharing of subformulas (RBC, BED)
- ▷ ...

Content

- ✓ Basics on SAT
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- ⇒ Basic SAT techniques
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Truth Tables

- ▷ **Exhaustive evaluation** of all subformulas:

φ_1	φ_2	$\varphi_1 \wedge \varphi_2$	$\varphi_1 \vee \varphi_2$	$\varphi_1 \rightarrow \varphi_2$	$\varphi_1 \leftrightarrow \varphi_2$
\perp	\perp	\perp	\perp	\top	\top
\perp	\top	\perp	\top	\top	\perp
\top	\perp	\perp	\top	\perp	\perp
\top	\top	\top	\top	\top	\top

- ▷ Requires **polynomial space**.
- ▷ Never used in practice
(100 variables $\Rightarrow > 10^{30}$ assignment $\Rightarrow > 10^{12}$ years assuming the evaluation of one assignment takes 1ns.)

Resolution [41, 12]

- ▷ **Search** for a refutation of φ
- ▷ φ is represented as a set of clauses
- ▷ Applies iteratively the **resolution rule** to pairs of clauses containing a conflicting literal, until a false clause is generated or the resolution rule is no more applicable
- ▷ Many different strategies

Resolution Rule

Resolution of a pair of clauses with exactly one incompatible variable:

$$\begin{array}{c}
 \underbrace{(l_1 \vee \dots \vee l_k)}_{\text{common}} \vee \underbrace{l}_{\text{resolvent}} \vee \underbrace{(l'_{k+1} \vee \dots \vee l'_m)}_{C'} \quad \quad \quad \underbrace{(l_1 \vee \dots \vee l_k)}_{\text{common}} \vee \underbrace{\neg l}_{\text{resolvent}} \vee \underbrace{(l''_{k+1} \vee \dots \vee l''_n)}_{C''} \\
 \hline
 \underbrace{(l_1 \vee \dots \vee l_k)}_{\text{common}} \vee \underbrace{(l'_{k+1} \vee \dots \vee l'_m)}_{C'} \vee \underbrace{(l''_{k+1} \vee \dots \vee l''_n)}_{C''}
 \end{array}$$

EXAMPLE:
$$\frac{(A \vee B \vee C \vee D \vee E) \quad (A \vee B \vee \neg C \vee F)}{(A \vee B \vee D \vee E \vee F)}$$

NOTE: many standard inference rules subcases of resolution:

$$\frac{A \rightarrow B \quad B \rightarrow C}{A \rightarrow C} \text{ (Transit.)} \quad \frac{A \quad A \rightarrow B}{B} \text{ (M. Ponens)} \quad \frac{\neg B \quad A \rightarrow B}{\neg A} \text{ (M. Tollens)}$$

Resolution Rules [12]: unit propagation

▷ Unit resolution:

$$\frac{\Gamma' \wedge (l) \wedge (\neg l \vee \bigvee_i l_i)}{\Gamma' \wedge (l) \wedge (\bigvee_i l_i)}$$

▷ Unit subsumption:

$$\frac{\Gamma' \wedge (l) \wedge (l \vee \bigvee_i l_i)}{\Gamma' \wedge (l)}$$

Applied **before** general resolution rule!

Resolution: basic strategy [12]

```

function Resolution( $\Gamma$ )
  if  $\perp \in \Gamma$                                      /* unsat */
    then return False;
  if (Resolve() is no more applicable to  $\Gamma$ )    /* sat    */
    then return True;
  if {a unit clause ( $l$ ) occurs in  $\Gamma$ }         /* unit   */
    then  $\Gamma := \textit{Unit\_Propagate}(l, \Gamma)$ ;
    return Resolution( $\Gamma$ )
   $v := \textit{select\_variable}(\Gamma)$ ;             /* resolve */
   $\Gamma = \Gamma \cup \bigcup_{v \in C', \neg v \in C''} \{\textit{Resolve}(C', C'') / \{C', C''\}\}$ ;
  return Resolution( $\Gamma$ )

```

Resolution: Examples

$$(A_1 \vee A_2) \quad (A_1 \vee \neg A_2) \quad (\neg A_1 \vee A_2) \quad (\neg A_1 \vee \neg A_2)$$

$$\Downarrow$$

$$(A_2) \quad (A_2 \vee \neg A_2) \quad (\neg A_2 \vee A_2) \quad (\neg A_2)$$

$$\Downarrow$$
$$\perp$$

\implies UNSAT

Resolution: Examples (cont.)

$$(A \vee B \vee C) \quad (B \vee \neg C \vee \neg F) \quad (\neg B \vee E)$$

$$\Downarrow$$

$$(A \vee C \vee E) \quad (\neg C \vee \neg F \vee E)$$

$$\Downarrow$$

$$(A \vee E \vee \neg F)$$

\implies SAT

Resolution: Examples

$$(A \vee B) \quad (A \vee \neg B) \quad (\neg A \vee C) \quad (\neg A \vee \neg C)$$

$$\Downarrow$$

$$(A) \quad (\neg A \vee C) \quad (\neg A \vee \neg C)$$

$$\Downarrow$$

$$(C) \quad (\neg C)$$

$$\Downarrow$$
$$\perp$$

\implies UNSAT

Resolution – summary

- ▷ Requires CNF
- ▷ Γ may blow up
 - ⇒ May require exponential space
- ▷ Not very much used in Boolean reasoning (unless integrated with DPLL procedure in recent implementations)

Semantic tableaux [47]

- ▷ **Search** for an assignment satisfying φ
- ▷ applies recursively **elimination rules** to the connectives
- ▷ If a branch contains A_i and $\neg A_i$, (ψ_i and $\neg\psi_1$) for some i , the branch is **closed**, otherwise it is **open**.
- ▷ if no rule can be applied to an **open** branch μ , then $\mu \models \varphi$;
- ▷ if all branches are **closed**, the formula is **not satisfiable**;

Tableau elimination rules

$$\frac{\varphi_1 \wedge \varphi_2}{\varphi_1}$$

$$\varphi_2$$

$$\frac{\neg(\varphi_1 \vee \varphi_2)}{\neg\varphi_1}$$

$$\neg\varphi_2$$

$$\frac{\neg(\varphi_1 \rightarrow \varphi_2)}{\varphi_1}$$

$$\neg\varphi_2$$

(\wedge -elimination)

$$\frac{\neg\neg\varphi}{\varphi}$$

($\neg\neg$ -elimination)

$$\frac{\varphi_1 \vee \varphi_2}{\varphi_1 \quad \varphi_2}$$

$$\frac{\neg(\varphi_1 \wedge \varphi_2)}{\neg\varphi_1 \quad \neg\varphi_2}$$

$$\frac{\varphi_1 \rightarrow \varphi_2}{\neg\varphi_1 \quad \varphi_2}$$

(\vee -elimination)

$$\frac{\varphi_1 \leftrightarrow \varphi_2}{\varphi_1 \quad \neg\varphi_1}$$

$$\varphi_2 \quad \neg\varphi_2$$

$$\frac{\neg(\varphi_1 \leftrightarrow \varphi_2)}{\varphi_1 \quad \neg\varphi_1}$$

$$\neg\varphi_2 \quad \varphi_2$$

(\leftrightarrow -elimination).

Semantic Tableaux – example

$$\varphi = (A_1 \vee A_2) \wedge (A_1 \vee \neg A_2) \wedge (\neg A_1 \vee A_2) \wedge (\neg A_1 \vee \neg A_2)$$

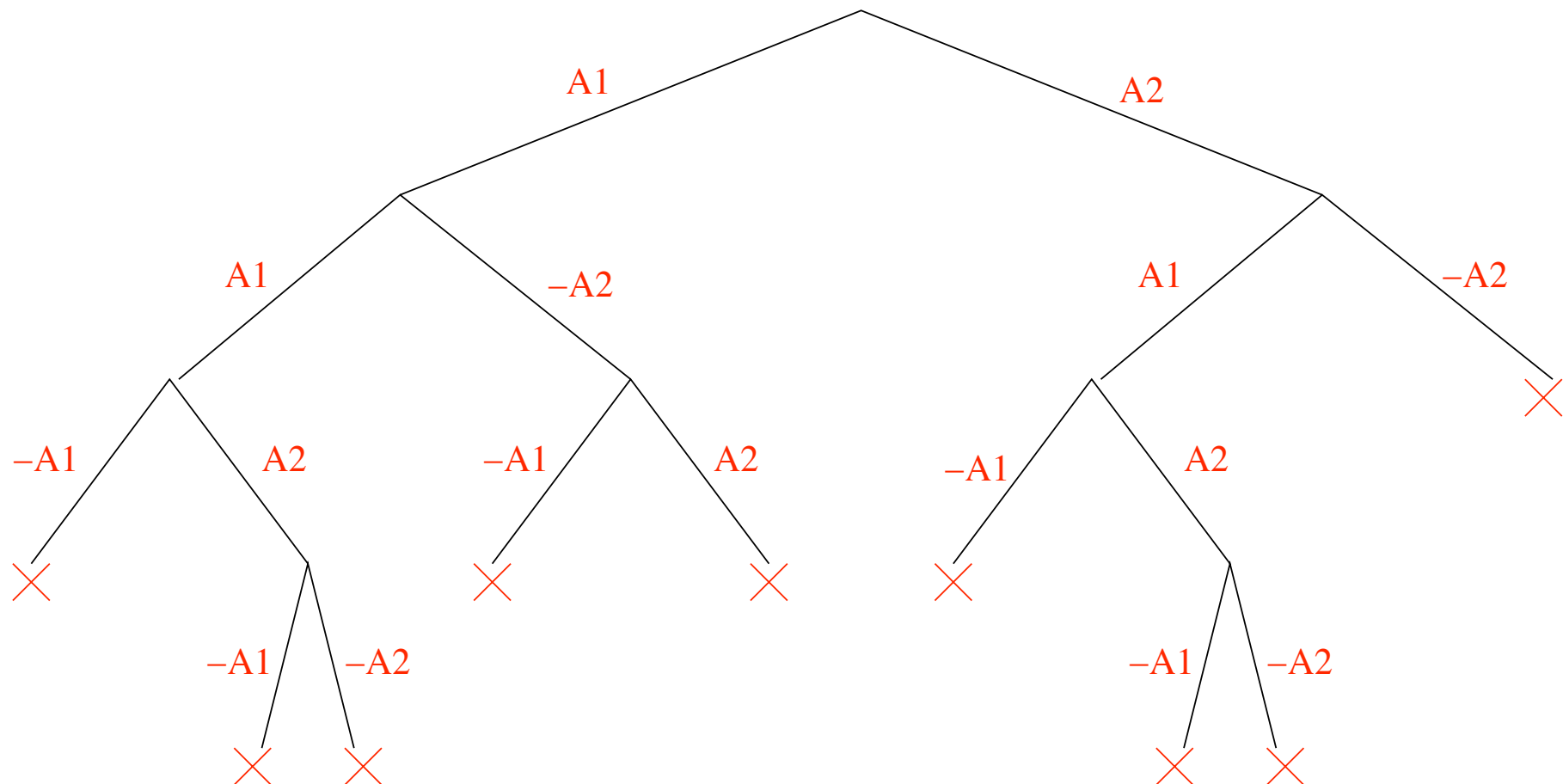


Tableau algorithm

```

function Tableau( $\Gamma$ )
  if  $A_i \in \Gamma$  and  $\neg A_i \in \Gamma$                                 /* branch closed */
    then return False;
  if  $(\varphi_1 \wedge \varphi_2) \in \Gamma$                                 /*  $\wedge$ -elimination */
    then return Tableau( $\Gamma \cup \{\varphi_1, \varphi_2\} \setminus \{(\varphi_1 \wedge \varphi_2)\}$ );
  if  $(\neg\neg\varphi_1) \in \Gamma$                                     /*  $\neg\neg$ -elimination */
    then return Tableau( $\Gamma \cup \{\varphi_1\} \setminus \{(\neg\neg\varphi_1)\}$ );
  if  $(\varphi_1 \vee \varphi_2) \in \Gamma$                                 /*  $\vee$ -elimination */
    then return Tableau( $\Gamma \cup \{\varphi_1\} \setminus \{(\varphi_1 \vee \varphi_2)\}$ ) or
               Tableau( $\Gamma \cup \{\varphi_2\} \setminus \{(\varphi_1 \vee \varphi_2)\}$ );
  ...
  return True;                                                /* branch expanded */

```

Semantic Tableaux – summary

- ▷ Handles all propositional formulas (CNF not required).
- ▷ Branches on disjunctions
- ▷ Intuitive, modular, easy to extend
⇒ loved by logicians.
- ▷ Rather inefficient
⇒ avoided by computer scientists.
- ▷ Requires polynomial space

DPLL [12, 11]

- ▷ **Davis-Putnam-Longeman-Loveland procedure** (DPLL)
- ▷ Tries to build an assignment μ satisfying φ ;
- ▷ At each step assigns a truth value to (all instances of) **one atom**.
- ▷ Performs **deterministic choices** first.

DPLL rules

$$\frac{\varphi_1 \wedge (l)}{\varphi_1[l|\top]} \text{ (Unit)}$$

$$\frac{\varphi}{\varphi[l|\top]} \text{ (l Pure)}$$

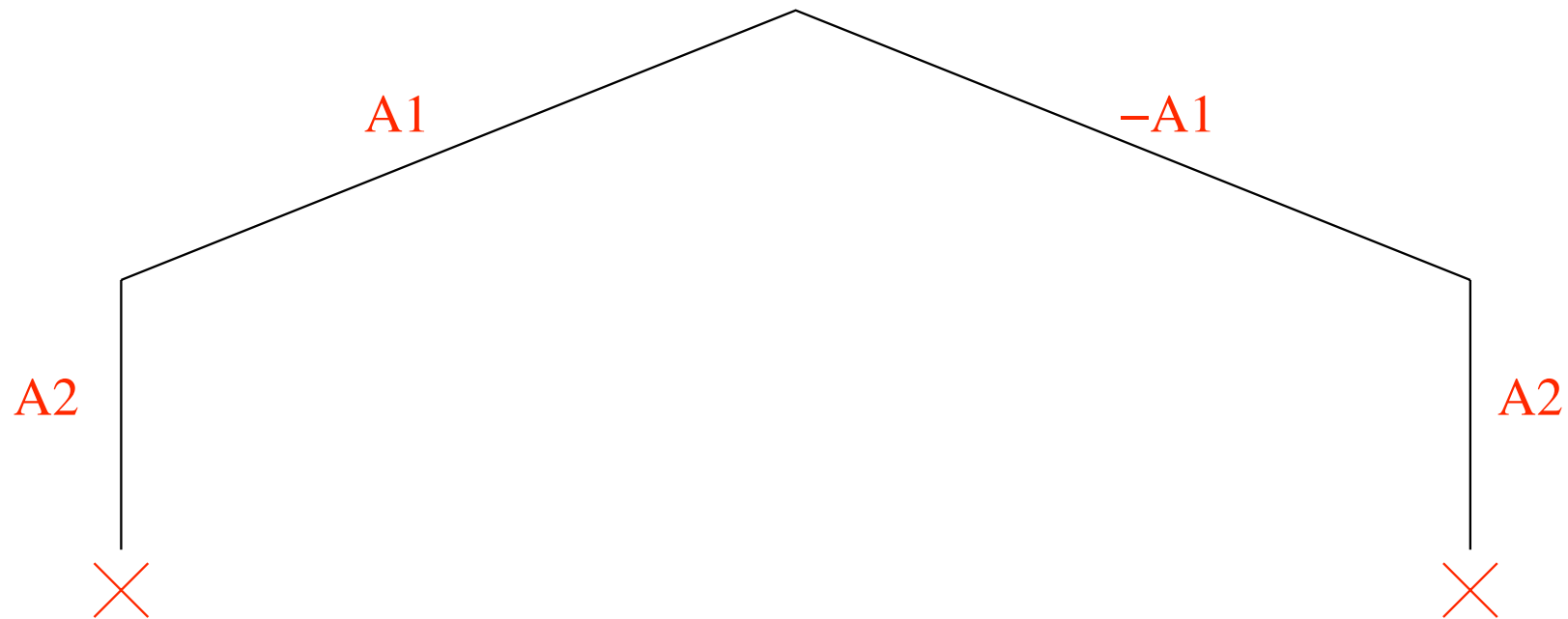
$$\frac{\varphi}{\varphi[l|\top] \quad \varphi[l|\perp]} \text{ (split)}$$

(l is a **pure literal** in φ iff it occurs **only positively**).

- Split applied **if and only if** the others cannot be applied.
- Richer formalisms described in [49, 37]

DPLL – example

$$\varphi = (A_1 \vee A_2) \wedge (A_1 \vee \neg A_2) \wedge (\neg A_1 \vee A_2) \wedge (\neg A_1 \vee \neg A_2)$$



DPLL Algorithm

```

function  $DPLL(\varphi, \mu)$ 
  if  $\varphi = \top$                                      /* base */
    then return True;
  if  $\varphi = \perp$                                    /* backtrack */
    then return False;
  if {a unit clause ( $l$ ) occurs in  $\varphi$ }           /* unit */
    then return  $DPLL(assign(l, \varphi), \mu \wedge l)$ ;
  if {a literal  $l$  occurs pure in  $\varphi$ }           /* pure */
    then return  $DPLL(assign(l, \varphi), \mu \wedge l)$ ;
   $l := choose\_literal(\varphi)$ ;                       /* split */
  return  $DPLL(assign(l, \varphi), \mu \wedge l)$  or
          $DPLL(assign(\neg l, \varphi), \mu \wedge \neg l)$ ;

```

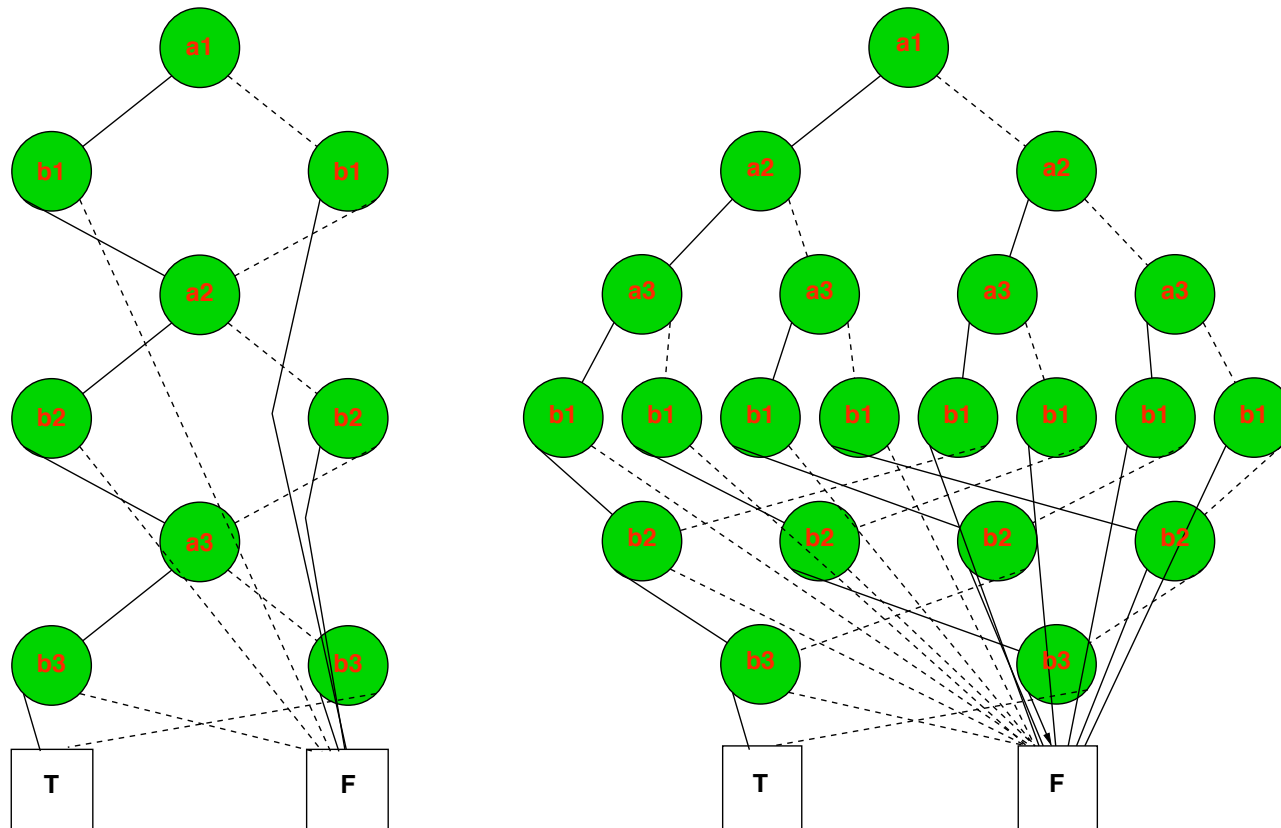
DPLL – summary

- ▷ Handles **CNF formulas** (non-CNF variant known [2, 22]).
- ▷ **Branches on truth values**
⇒ all instances of an atom assigned simultaneously
- ▷ **Postpones branching as much as possible.**
- ▷ Mostly ignored by logicians.
- ▷ Probably **the most efficient SAT algorithm**
⇒ loved by computer scientists.
- ▷ Requires **polynomial space**
- ▷ **Choose_literal()** critical!
- ▷ Many very efficient implementations [52, 46, 4, 36].

Ordered Binary Decision Diagrams (OBDDs) [8]

- ▷ “If-then-else” binary DAGs with two leaves: **1** and **0**
- ▷ Paths leading to **1** represent **models**
Paths leading to **0** represent **counter-models**
- ▷ **Variable ordering** A_1, A_2, \dots, A_n imposed a priori.

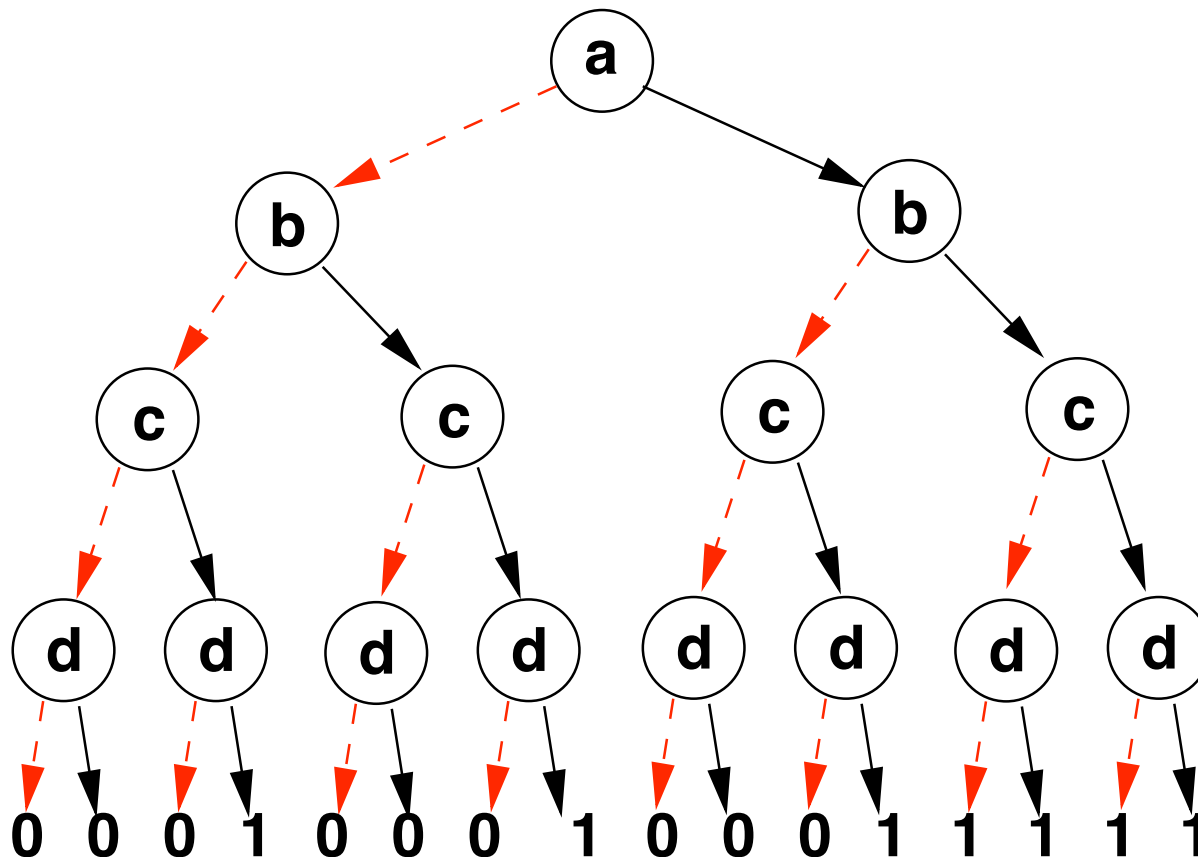
OBDD - Examples



OBDDs of $(a_1 \leftrightarrow b_1) \wedge (a_2 \leftrightarrow b_2) \wedge (a_3 \leftrightarrow b_3)$ with different variable orderings

Ordered Decision Trees

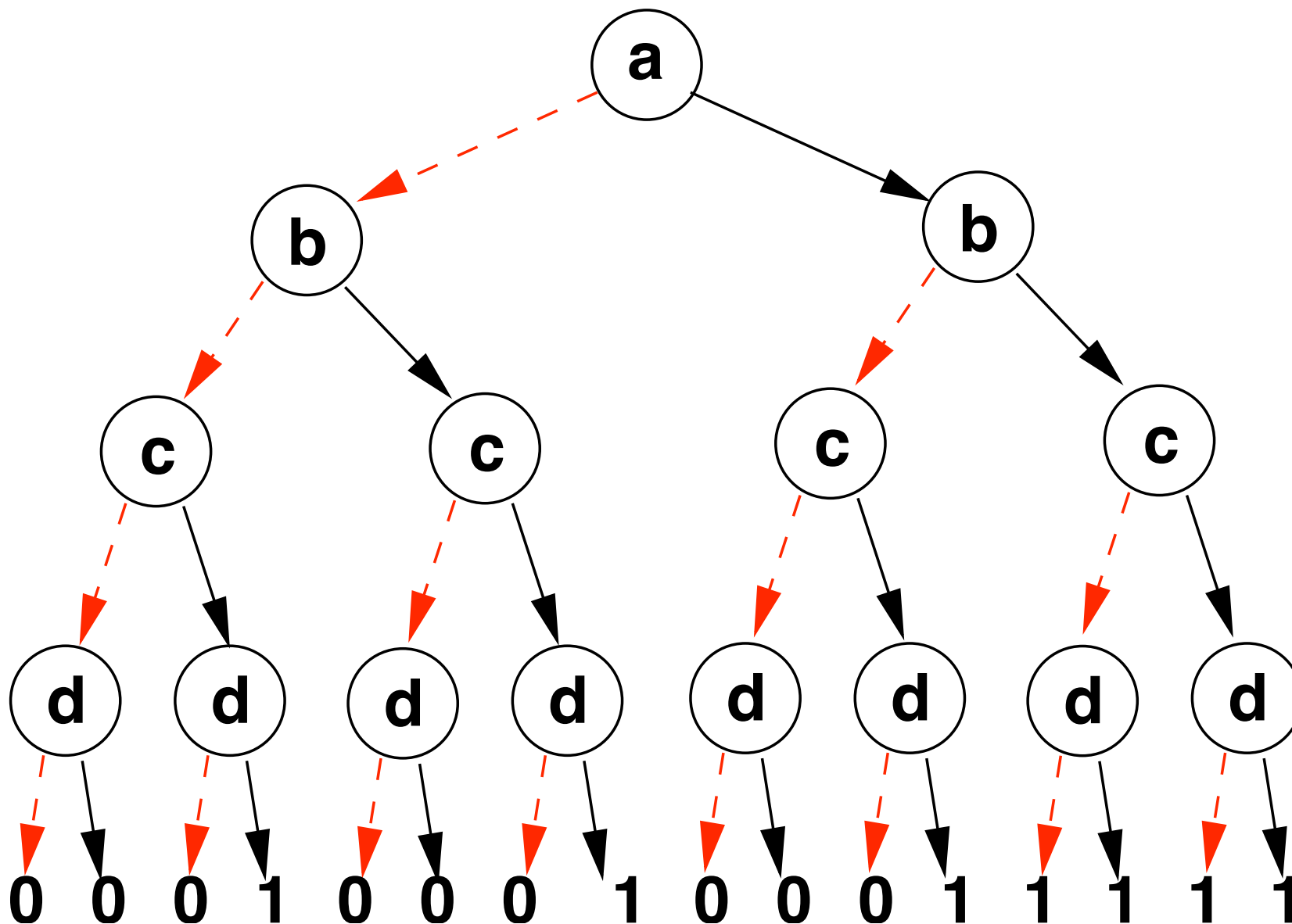
- ▷ **Ordered Decision Tree**: from root to leaves, variables are encountered always in the same order
- ▷ Example: Ordered Decision tree for $\varphi = (a \wedge b) \vee (c \wedge d)$



From Ordered Decision Trees to OBDD's: reductions

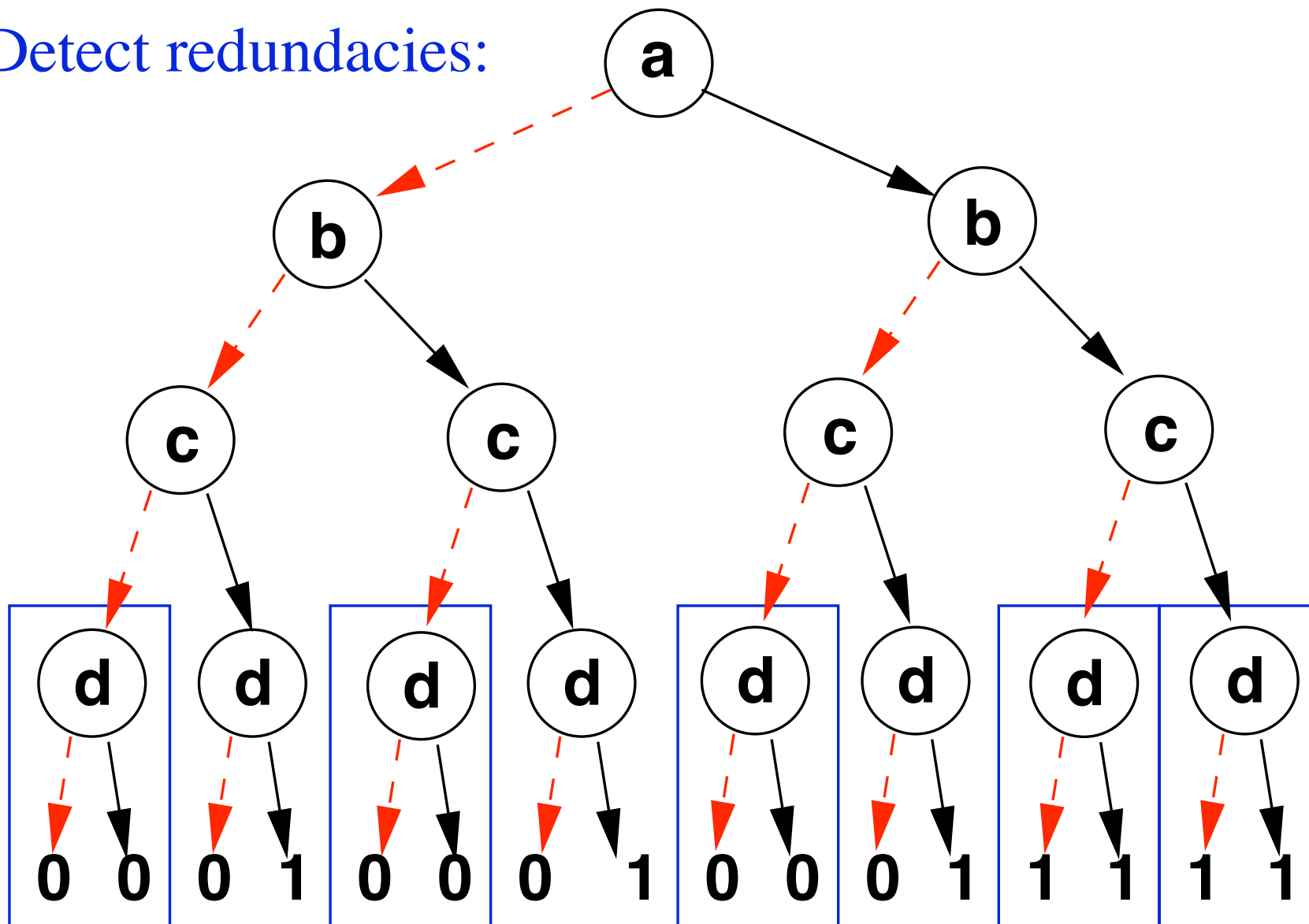
- ▷ Recursive applications of the following **reductions**:
 - **share subnodes**: point to the same occurrence of a subtree
 - **remove redundancies**: nodes with same left and right children can be eliminated

Reduction: example



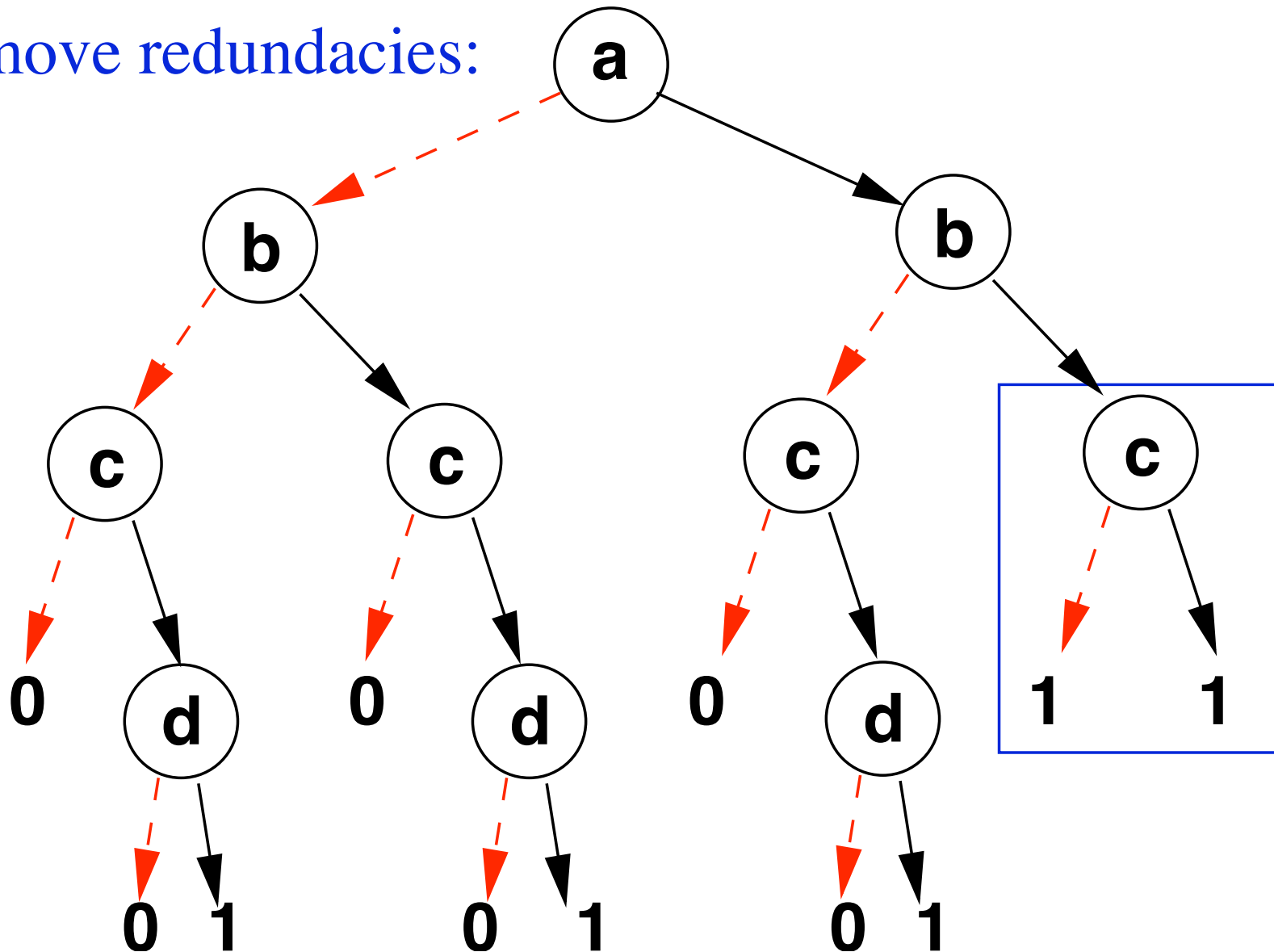
Reduction: example [cont.]

Detect redundancies:



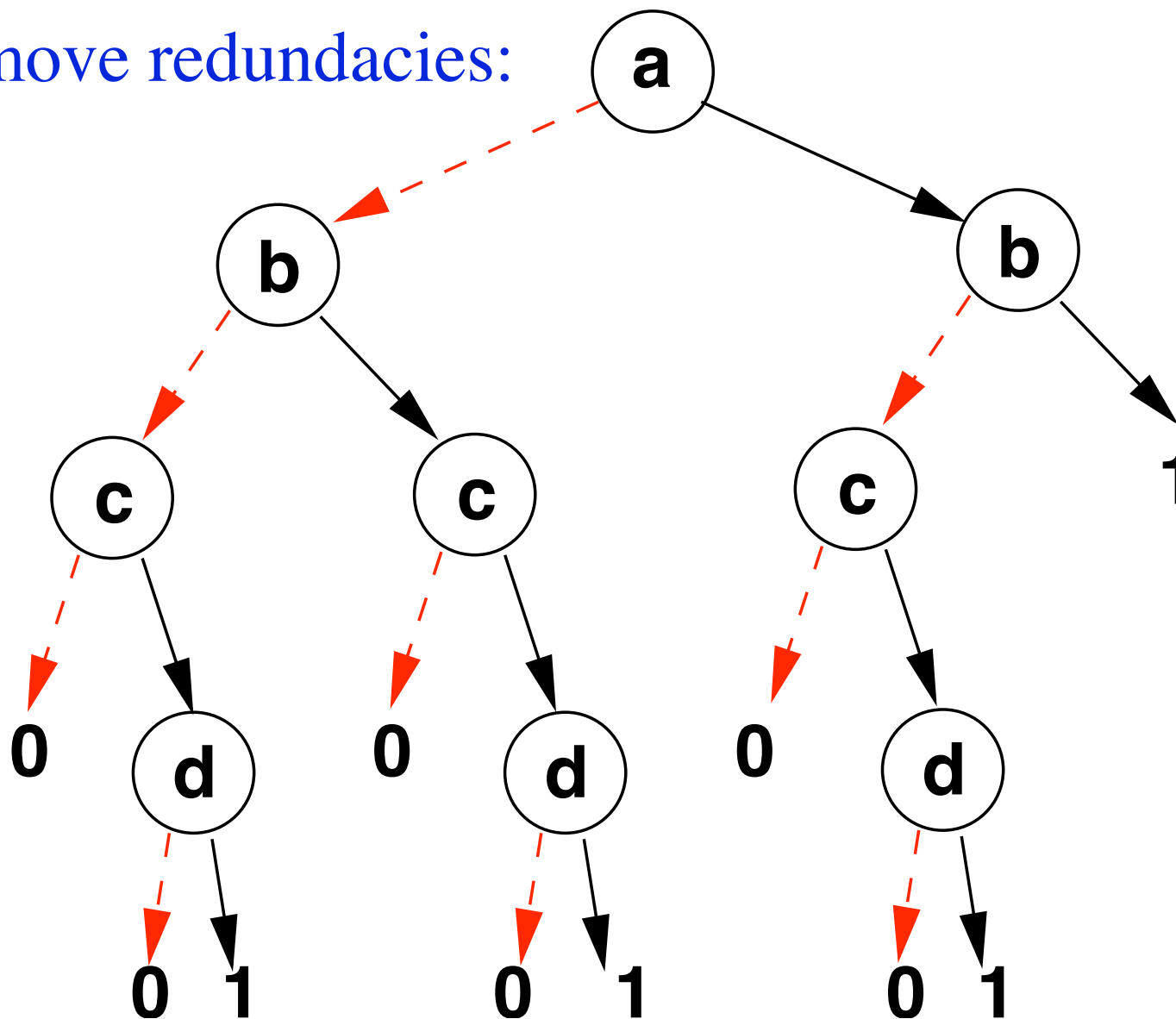
Reduction: example [cont.]

Remove redundancies:



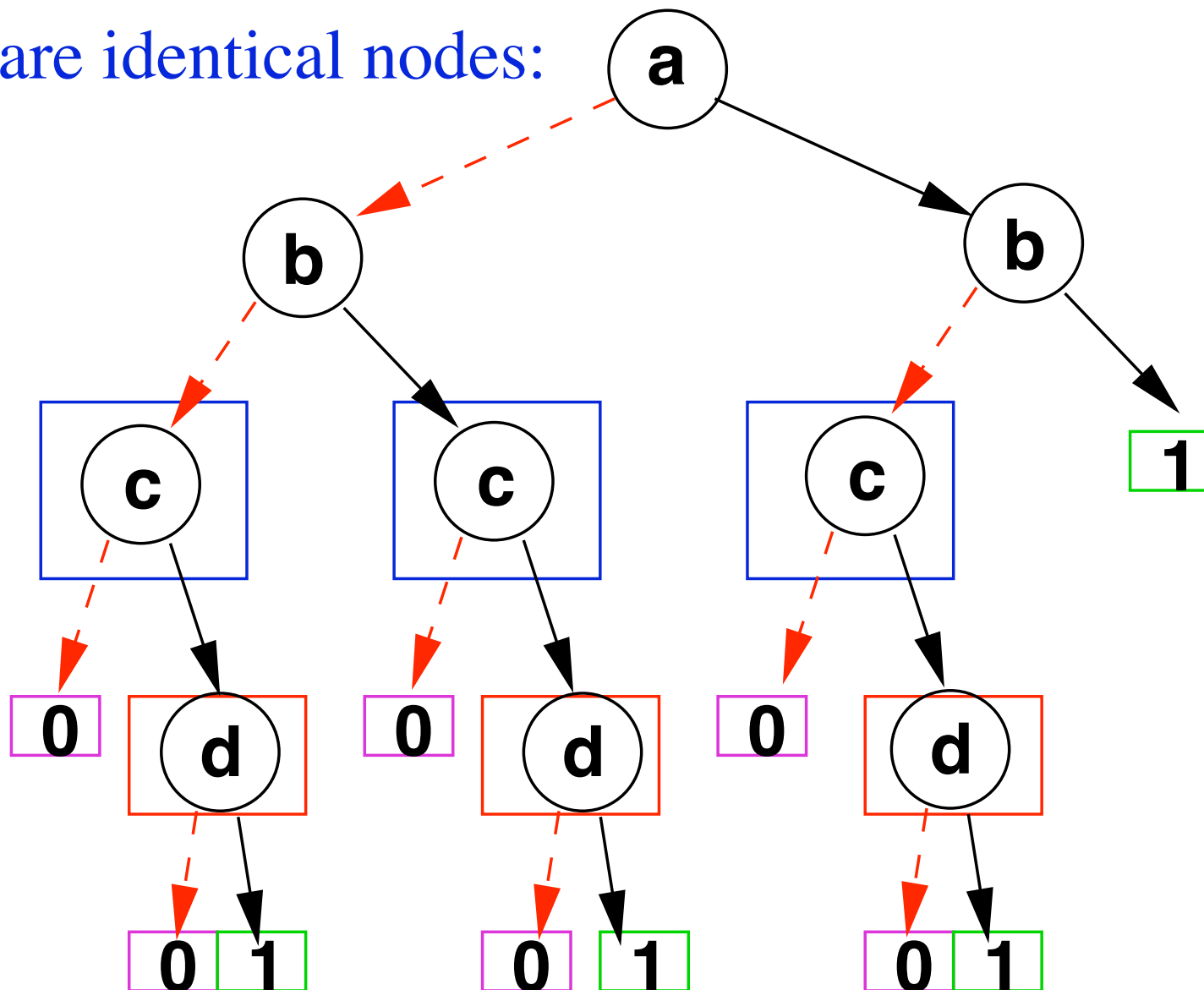
Reduction: example [cont.]

Remove redundancies:



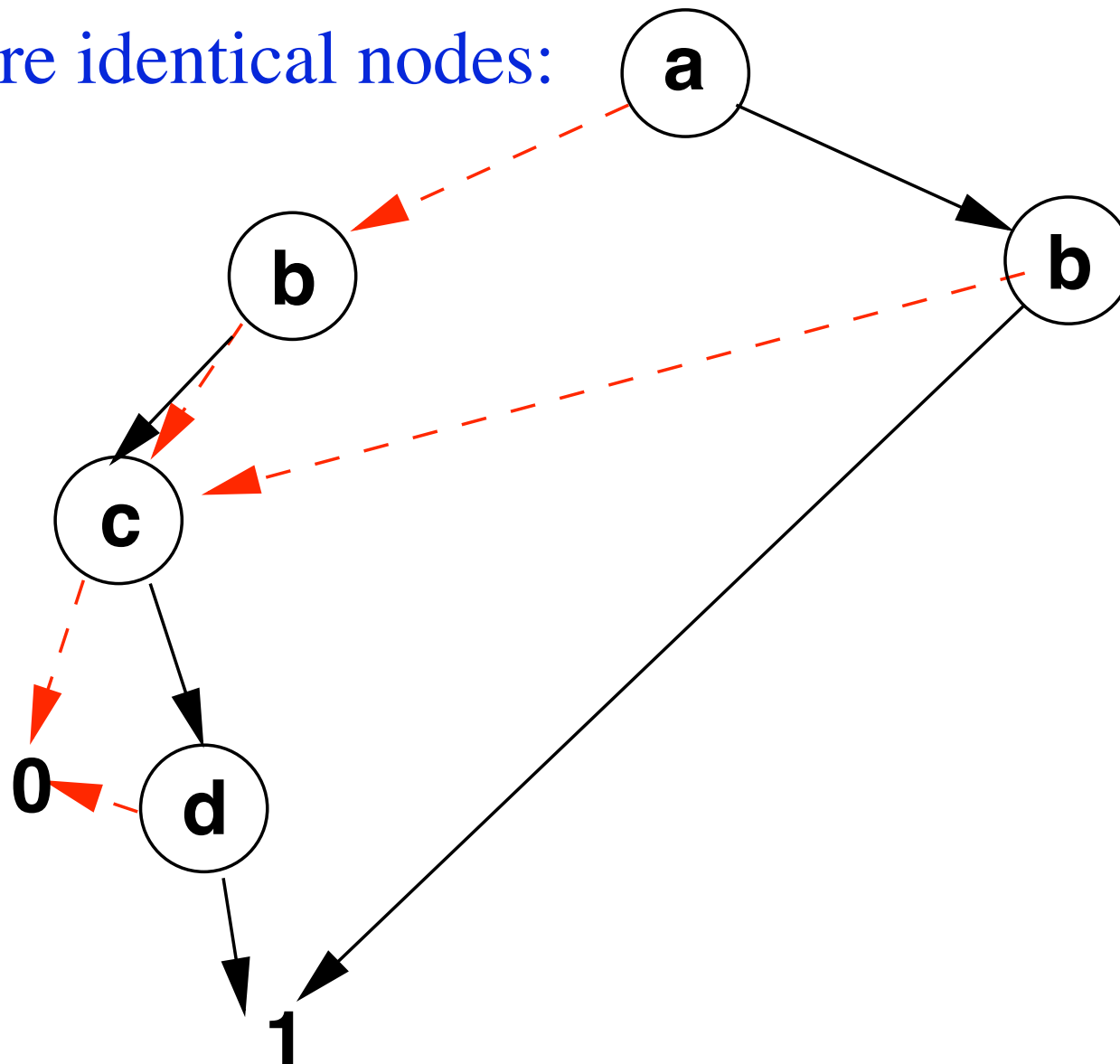
Reduction: example [cont.]

Share identical nodes:



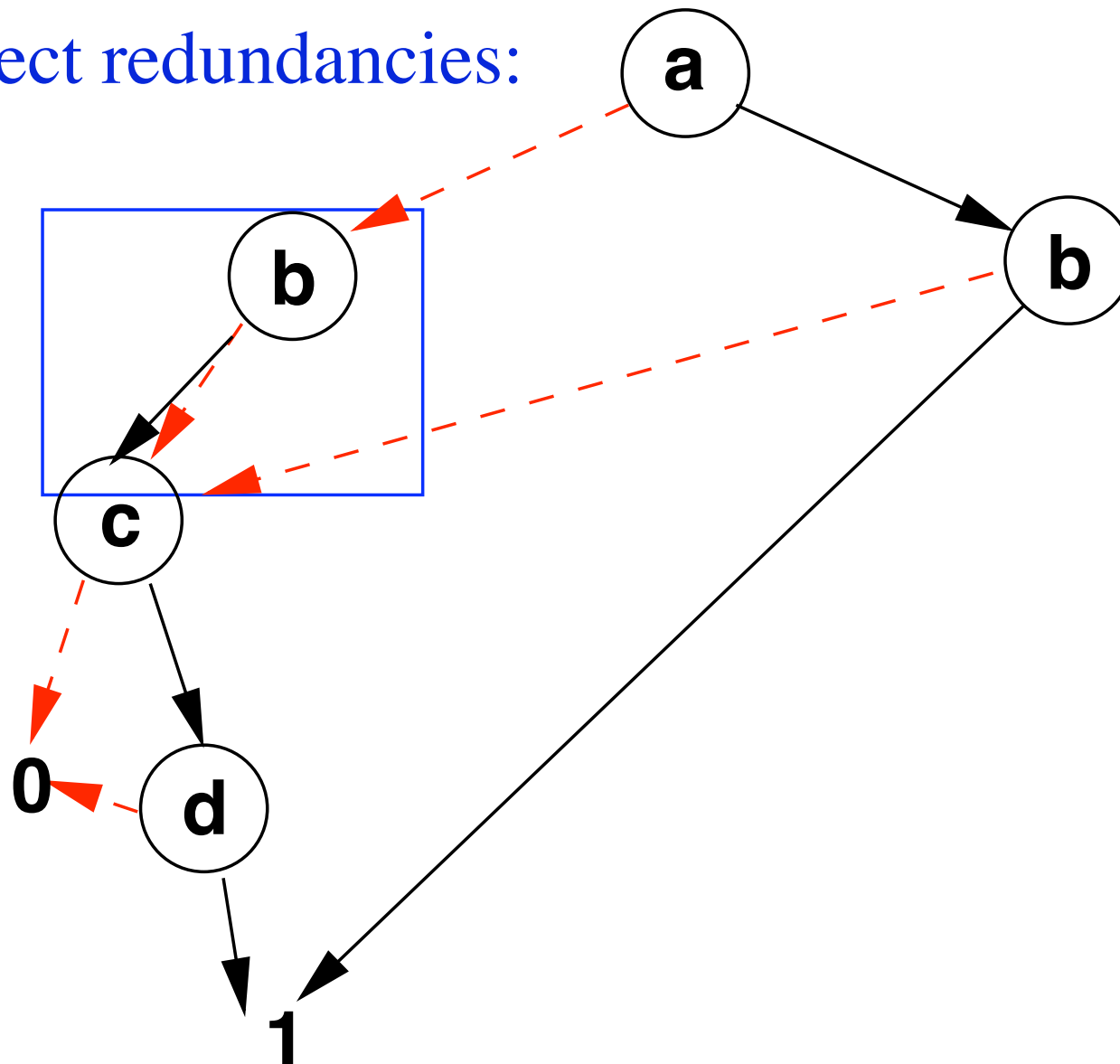
Reduction: example [cont.]

Share identical nodes:



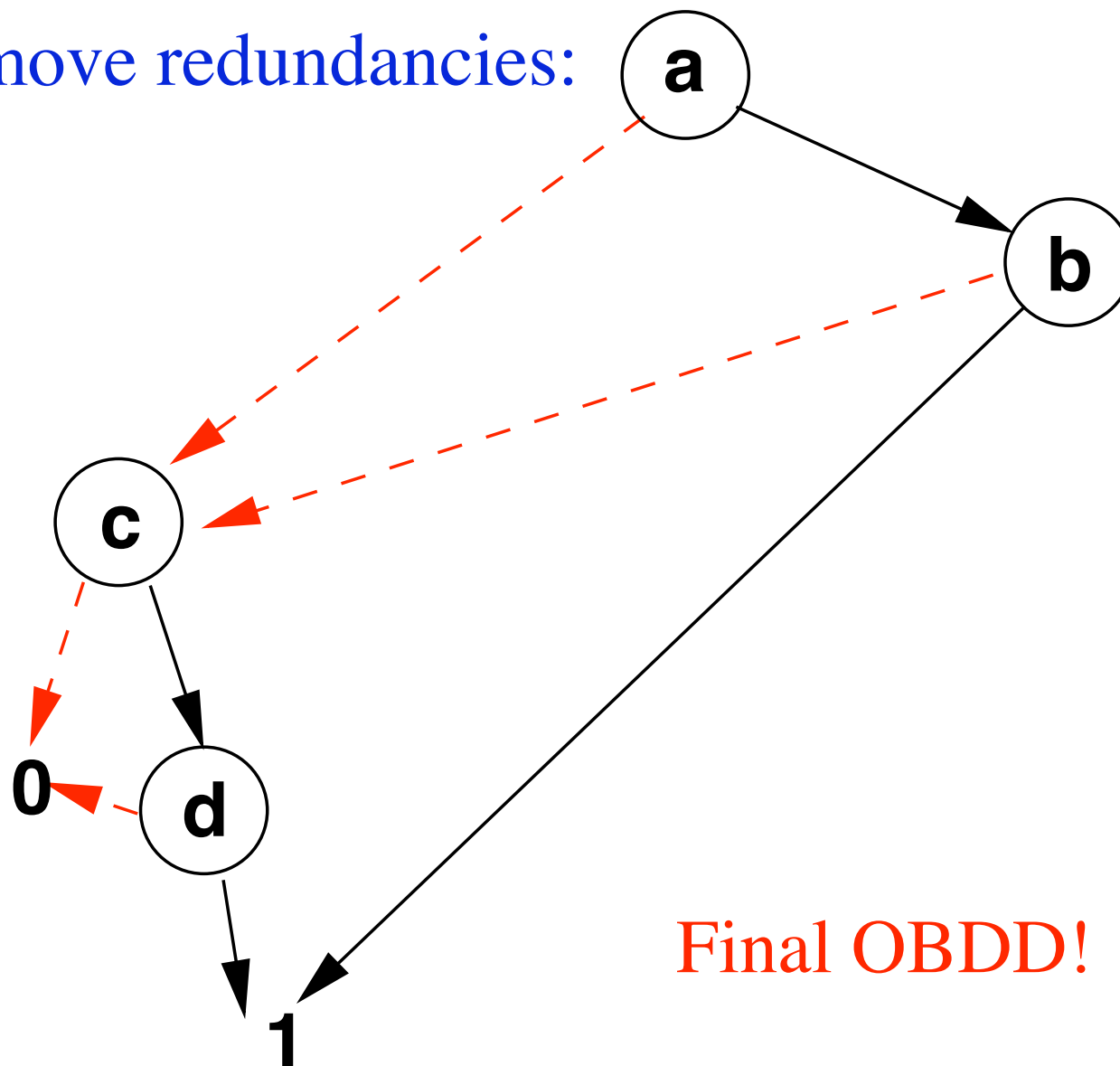
Reduction: example [cont.]

Detect redundancies:



Reduction: example [cont.]

Remove redundancies:



Recursive structure of an OBDD

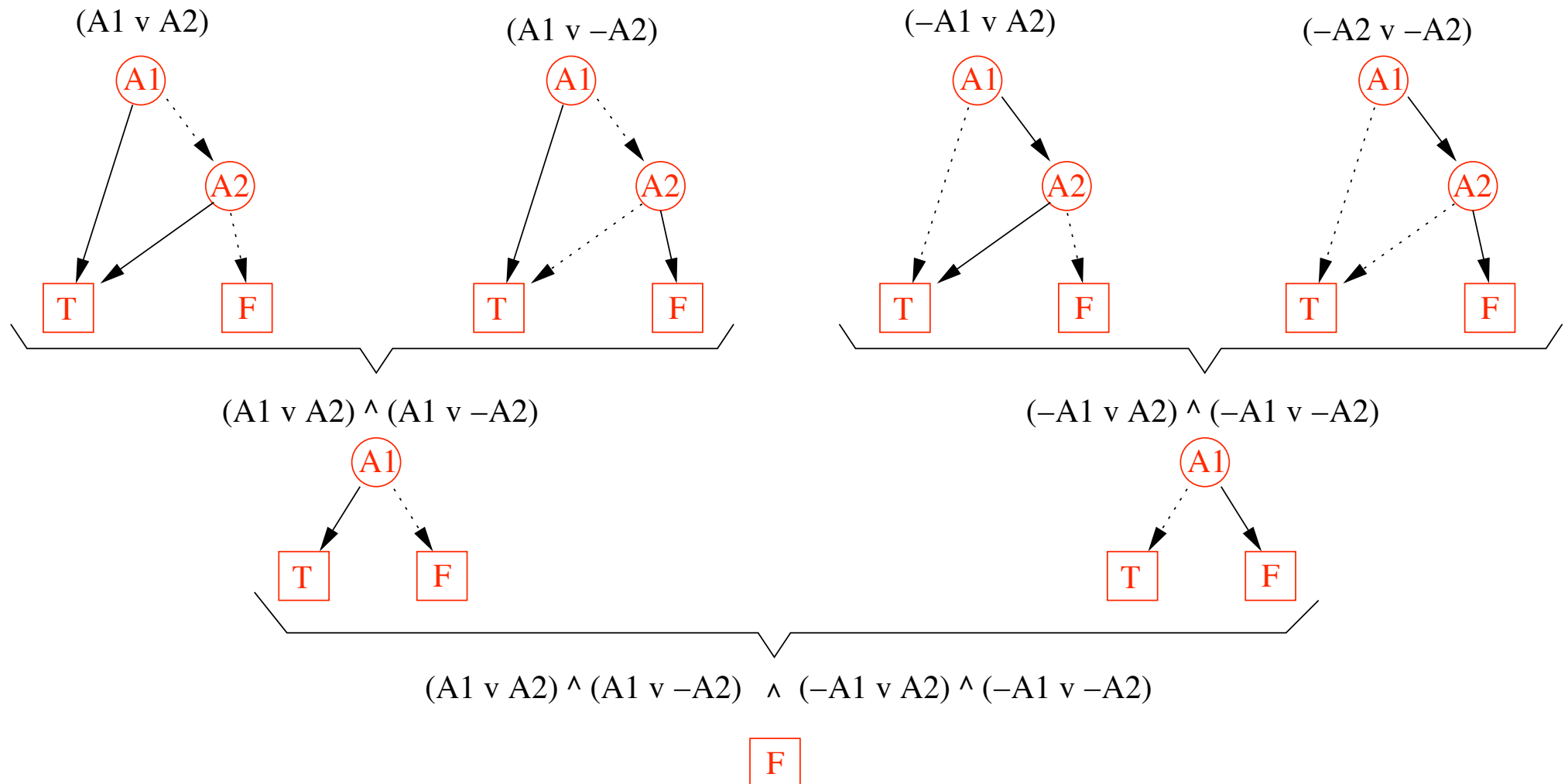
- ▷ $OBDD(\top, \{\dots\}) = 1$,
- ▷ $OBDD(\perp, \{\dots\}) = 0$,
- ▷ $OBDD(\varphi, \{A_1, A_2, \dots, A_n\}) =$
if A_1
then $OBDD(\varphi[A_1|\top], \{A_2, \dots, A_n\})$
else $OBDD(\varphi[A_1|\perp], \{A_2, \dots, A_n\})$

Incrementally building an OBDD

- ▷ $obdd_build(\top, \{\dots\}) := 1,$
 - ▷ $obdd_build(\perp, \{\dots\}) := 0,$
 - ▷ $obdd_build((\varphi_1 \text{ op } \varphi_2), \{A_1, \dots, A_n\}) :=$
 $reduce($
 $obdd_merge(\text{ op},$
 $obdd_build(\varphi_1, \{A_1, \dots, A_n\}),$
 $obdd_build(\varphi_2, \{A_1, \dots, A_n\}),$
 $\{A_1, \dots, A_n\}$
 $))$
- $op \in \{\wedge, \vee, \rightarrow, \leftrightarrow\}$

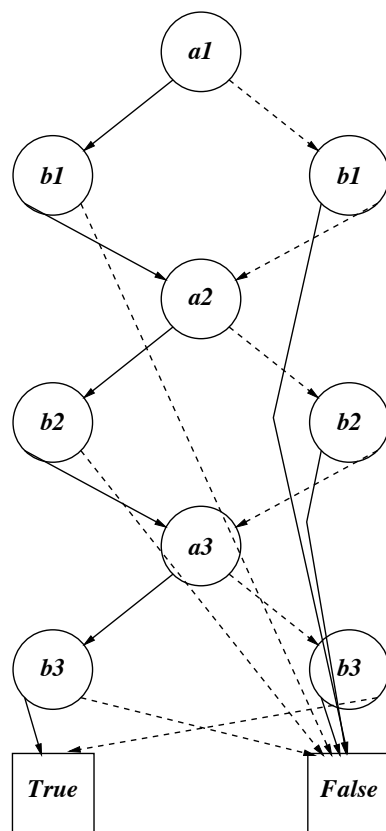
OBDD incremental building – example

$$\varphi = (A_1 \vee A_2) \wedge (A_1 \vee \neg A_2) \wedge (\neg A_1 \vee A_2) \wedge (\neg A_1 \vee \neg A_2)$$

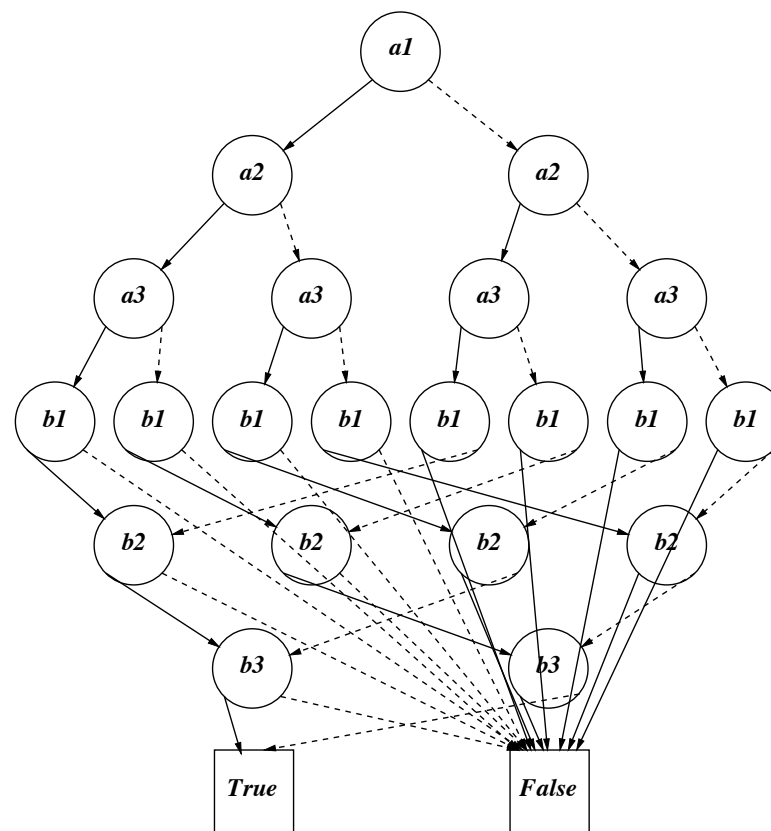


Critical choice of variable Orderings in OBDD's

$$\varphi = (a1 \leftarrow b1) \wedge (a2 \leftarrow b2) \wedge (a3 \leftarrow b3)$$



Linear size



Exponential size

OBDD's as canonical representation of Boolean formulas

- ▷ An OBDD is a **canonical representation** of a Boolean formula: once the variable ordering is established, equivalent formulas are represented by the same OBDD:

$$\varphi_1 \leftrightarrow \varphi_2 \iff OBDD(\varphi_1) = OBDD(\varphi_2)$$

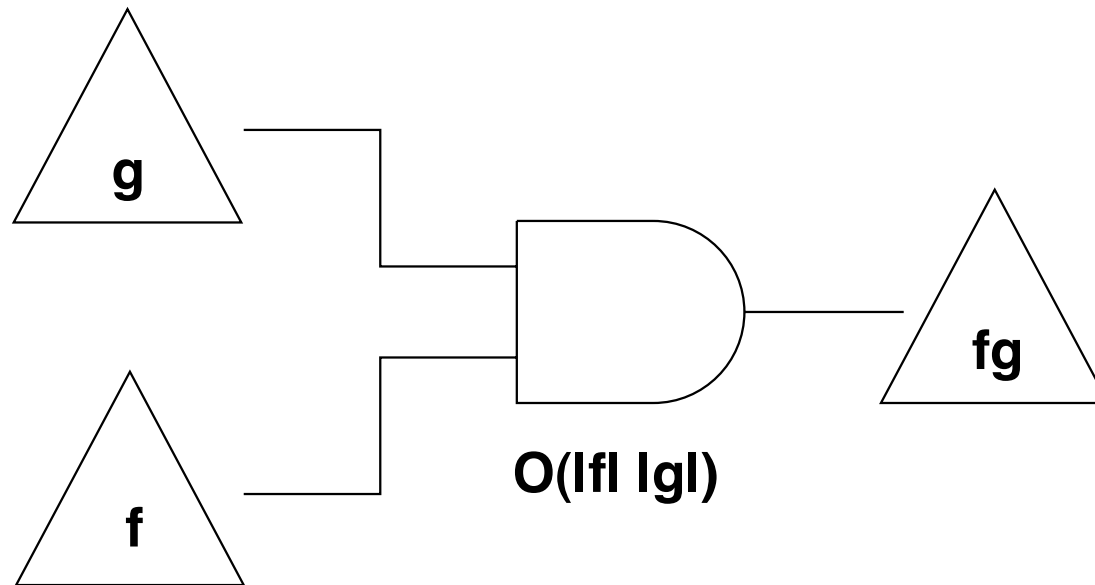
- ▷ equivalence check requires constant time!
 - ⇒ validity check requires constant time! ($\varphi \leftrightarrow \top$)
 - ⇒ (un)satisfiability check requires constant time! ($\varphi \leftrightarrow \perp$)
- ▷ the set of the paths from the root to 1 represent all the models of the formula
- ▷ the set of the paths from the root to 0 represent all the counter-models of the formula

Exponentiality of OBDD's

- ▷ The size of OBDD's may grow exponentially wrt. the number of variables in worst-case
- ▷ Consequence of the canonicity of OBDD's (unless $P = \text{co-NP}$)
- ▷ Example: there exist no polynomial-size OBDD representing the electronic circuit of a bitwise multiplier
- ▷ N.B.: the size of intermediate OBDD's may be bigger than that of the final one (e.g., inconsistent formula)

Useful Operations over OBDDs

- ▷ the **equivalence check** between two OBDDs is simple
 - are they the same OBDD? (\implies constant time)
- ▷ the size of a **Boolean composition** is up to the product of the size of the operands: $|f \text{ op } g| = O(|f| \cdot |g|)$



(but typically much smaller on average).

Boolean quantification

▷ If v is a Boolean variable, then

$$\exists v.f \quad := \quad f|_{v=0} \vee f|_{v=1}$$

$$\forall v.f \quad := \quad f|_{v=0} \wedge f|_{v=1}$$

▷ Multi-variable quantification: $\exists(w_1, \dots, w_n).f \quad := \quad \exists w_1 \dots \exists w_n.f$

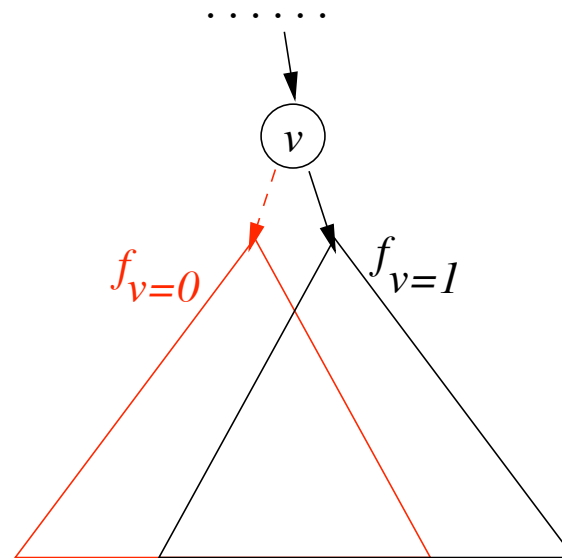
▷ Example: $\exists(b, c).((a \wedge b) \vee (c \wedge d)) = a \vee d$

▷ naive expansion of quantifiers to propositional logic may cause a blow-up in size of the formulae

▷ OBDD's handle very efficiently quantification operations

OBDD's and Boolean quantification

- ▶ OBDD's handle quantification operations rather efficiently
 - if f is a sub-OBDD labeled by variable v , then $f|_{v=1}$ and $f|_{v=0}$ are the “then” and “else” branches of f



⇒ lots of sharing of subformulae!

OBDD – summary

- ▷ **Factorize** common parts of the search tree (DAG)
- ▷ Require setting a **variable ordering** a priori (**critical!**)
- ▷ **Canonical representation** of a Boolean formula.
- ▷ Once built, logical operations (satisfiability, validity, equivalence) immediate.
- ▷ Represents **all** models and counter-models of the formula.
- ▷ Require **exponential space** in worst-case
- ▷ **Very efficient** for some practical problems (circuits, symbolic model checking).

Incomplete SAT techniques: GSAT, WSAT [45, 44]

- ▷ **Hill-Climbing techniques:** GSAT, WSAT
- ▷ looks for a **complete** assignment;
- ▷ starts from a random assignment;
- ▷ **Greedy** search: looks for a better “neighbor” assignment
- ▷ **Avoid local minima:** restart & random walk

The GSAT algorithm

```
function GSAT( $\varphi$ )
  for  $i := 1$  to Max-tries do
     $\mu :=$  rand-assign( $\varphi$ );
    for  $j := 1$  to Max-flips do
      if ( $score(\varphi, \mu) = 0$ )
        then return True;
      else Best-flips := hill-climb( $\varphi, \mu$ );
          $A_i :=$  rand-pick(Best-flips);
          $\mu :=$  flip( $A_i, \mu$ );
    end
  end
end
return “no satisfying assignment found”.
```


The WalkSAT algorithm

Slide contributed by the student Silvia Tomasi

```
WALKSAT( $\varphi$ , MAX-STEPS, MAX-TRIES, select())  
1  for  $i \leftarrow 1$  to MAX-TRIES  
2  do  $\mu \leftarrow$  a randomly generated truth assignment;  
3    for  $j \leftarrow 1$  to MAX-STEPS  
4    do if  $\mu$  satisfies  $\varphi$   
5      then return  $\mu$ ;  
6      else  $C \leftarrow$  randomly selected clause unsatisfied under  $\mu$ ;  
7           $x \leftarrow$  variable selected from  $C$  according to heuristic select();  
8           $\mu \leftarrow \mu$  with  $x$  flipped;  
9  return error "no solution found"
```

GSAT & WSAT– summary

- ▷ Handle only CNF formulas.
- ▷ Incomplete
- ▷ Extremely efficient for some (satisfiable) problems.
- ▷ Require polynomial space
- ▷ Non-CNF Variants: NC-GSAT [42], DAG-SAT [43]

Content

- ✓ Basics on SAT
- ✓ NNF, CNF and conversions
- ✓ Basic SAT techniques
- ⇒ **Modern SAT Solvers**
 - Advanced Functionalities: proofs, unsat cores, interpolants

Variants of DPLL

DPLL is a **family** of algorithms.

- ▷ preprocessing: (subsumption, 2-simplification, resolution)
- ▷ different branching heuristics
- ▷ backjumping
- ▷ learning
- ▷ restarts
- ▷ (horn relaxation)
- ▷ ...

Modern DPLL implementations [46, 4, 55, 23]

- ▷ Non-recursive: stack-based representation of data structures
- ▷ Efficient data structures for doing and undoing assignments
- ▷ Perform non-chronological backtracking and learning
- ▷ May perform search restarts
- ▷ Reason on total assignments

Dramatically efficient: solve industrial-derived problems with $\approx 10^7$ Boolean variables and $\approx 10^7$ clauses

Iterative description of DPLL [46, 55]

```

Function DPLL (formula:  $\varphi$ , assignment &  $\mu$ ) {
  status := preprocess( $\varphi, \mu$ );
  while (1) {
    decide_next_branch( $\varphi, \mu$ );
    while (1) {
      status := deduce( $\varphi, \mu, \eta$ );    $\eta$  is a conflict set
      if (status == Sat)
        return Sat;
      if (status == Conflict) {
        blevel := analyze_conflict( $\varphi, \mu, \eta$ );
        if (blevel == 0)
          return Unsat;
        else backtrack(blevel,  $\varphi, \mu$ );
      }
    }
  }
}

```

Iterative description of DPLL [46, 55]

- ▷ `preprocess`(φ, μ) simplifies φ into an easier equisatisfiable formula (and updates μ if it is the case)
- ▷ `decide_next_branch`(φ, μ) chooses a new decision literal from φ according to some heuristic, and adds it to μ
- ▷ `deduce`(φ, μ, η) performs all deterministic assignments (unit), and updates φ, μ accordingly. If this causes a conflict, η is the subset of μ causing the conflict (conflict set).
- ▷ `analyze_conflict`(φ, μ, η) returns the “wrong-decision” level suggested by η (“0” means that a conflict exists even without branching)
- ▷ `backtrack`(`blevel`, φ, μ) undoes the branches up to `blevel`, and updates φ, μ accordingly

Techniques to achieve efficiency in DPLL

- ▷ **Preprocessing**: preprocess the input formula so that to make it easier to solve
- ▷ **Look-ahead**: exploit information about the remaining search space
 - unit propagation
 - forward checking (branching heuristics)
- ▷ **Look-back**: exploit information about search which has already taken place
 - Backjumping & learning
- ▷ Others
 - restarts
 - ...

Preprocessing: (sorting plus) subsumption

▷ Detect and remove subsumed clauses:

$$\varphi_1 \wedge (l_2 \vee l_1) \wedge \varphi_2 \wedge (l_2 \vee l_3 \vee l_1) \wedge \varphi_3$$

⇓

$$\varphi_1 \wedge (l_1 \vee l_2) \wedge \varphi_2 \wedge \varphi_3$$

Preprocessing: detect & collapse equivalent literals [7]

Repeat:

1. build the **implication graph** induced by binary clauses
2. detect **strongly connected cycles** \implies **equivalence classes of literals**
3. perform substitutions
4. perform unit and pure literal.

Until (no more simplification is possible).

▷ Ex:

$$\varphi_1 \wedge (\neg l_2 \vee l_1) \wedge \varphi_2 \wedge (\neg l_3 \vee l_2) \wedge \varphi_3 \wedge (\neg l_1 \vee l_3) \wedge \varphi_4$$

$$\Downarrow l_1 \leftrightarrow l_2 \leftrightarrow l_3$$

$$(\varphi_1 \wedge \varphi_2 \wedge \varphi_3 \wedge \varphi_4)[l_2 \leftarrow l_1; l_3 \leftarrow l_1;]$$

▷ Very effective in many application domains.

Preprocessing: resolution (and subsumption) [3]

- ▷ Apply some basic steps of resolution (and simplify):

$$\varphi_1 \wedge (l_2 \vee l_1) \wedge \varphi_2 \wedge (l_2 \vee \neg l_1) \wedge \varphi_3$$

$\Downarrow_{\text{resolve}}$

$$\varphi_1 \wedge (l_2) \wedge \varphi_2 \wedge \varphi_3$$

$\Downarrow_{\text{unit-propagate}}$

$$(\varphi_1 \wedge \varphi_2 \wedge \varphi_3)[l_2 \leftarrow \top]$$

Branching heuristics

- ▷ **Branch** is the source of non-determinism for DPLL
 - ⇒ critical for efficiency
- ▷ many branch heuristics conceived in literature.

Some example heuristics

- ▷ **MOMS** heuristics: pick the literal occurring **m**ost **o**ften in the **m**inimal **s**ize clauses

⇒ fast and simple, many variants

- ▷ **Jeroslow-Wang**: choose the literal with maximum

$$\text{score}(l) := \sum_{l \in c \ \& \ c \in \varphi} 2^{-|c|}$$

⇒ estimates l 's contribution to the satisfiability of φ

- ▷ **Satz** [29]: selects a candidate set of literals, perform unit propagation, chooses the one leading to smaller clause set

⇒ maximizes the effects of unit propagation

- ▷ **VSIDS** [36]: **v**ariable **s**tate **i**ndependent **d**ecaying **s**um

- “static”: scores updated only at the end of a branch
- “local”: privileges variable in recently learned clauses

“Classic” chronological backtracking

- ▷ variable assignments (literals) stored in a stack
- ▷ each variable assignments labeled as “unit”, “open”, “closed”
- ▷ when a conflict is encountered, the stack is popped up to the most recent open assignment l
- ▷ l is toggled, is labeled as “closed”, and the search proceeds.

Classic chronological backtracking – example (1)

$$c_1 : \neg A_1 \vee A_2$$

$$c_2 : \neg A_1 \vee A_3 \vee A_9$$

$$c_3 : \neg A_2 \vee \neg A_3 \vee A_4$$

$$c_4 : \neg A_4 \vee A_5 \vee A_{10}$$

$$c_5 : \neg A_4 \vee A_6 \vee A_{11}$$

$$c_6 : \neg A_5 \vee \neg A_6$$

$$c_7 : A_1 \vee A_7 \vee \neg A_{12}$$

$$c_8 : A_1 \vee A_8$$

$$c_9 : \neg A_7 \vee \neg A_8 \vee \neg A_{13}$$

...

Classic chronological backtracking – example (2)

$$c_1 : \neg A_1 \vee A_2$$

$$c_2 : \neg A_1 \vee A_3 \vee A_9$$

$$c_3 : \neg A_2 \vee \neg A_3 \vee A_4$$

$$c_4 : \neg A_4 \vee A_5 \vee A_{10}$$

$$c_5 : \neg A_4 \vee A_6 \vee A_{11}$$

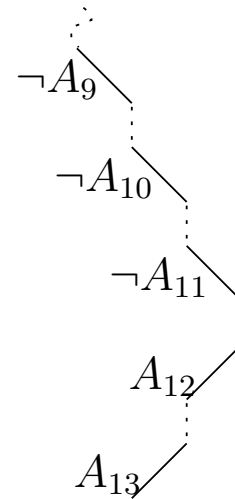
$$c_6 : \neg A_5 \vee \neg A_6$$

$$c_7 : A_1 \vee A_7 \vee \neg A_{12}$$

$$c_8 : A_1 \vee A_8$$

$$c_9 : \neg A_7 \vee \neg A_8 \vee \neg A_{13}$$

...



$\{\dots, \neg A_9, \neg A_{10}, \neg A_{11}, A_{12}, A_{13}, \dots\}$

(initial assignment)

Classic chronological backtracking – example (3)

$$c_1 : \neg A_1 \vee A_2$$

$$c_2 : \neg A_1 \vee A_3 \vee A_9$$

$$c_3 : \neg A_2 \vee \neg A_3 \vee A_4$$

$$c_4 : \neg A_4 \vee A_5 \vee A_{10}$$

$$c_5 : \neg A_4 \vee A_6 \vee A_{11}$$

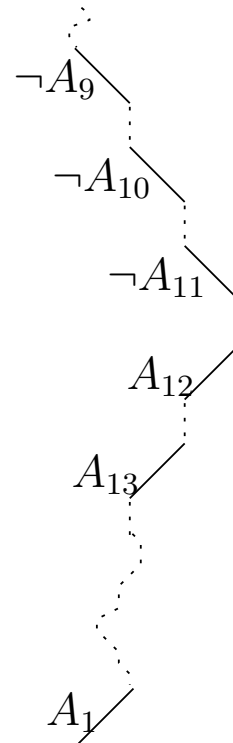
$$c_6 : \neg A_5 \vee \neg A_6$$

$$c_7 : A_1 \vee A_7 \vee \neg A_{12} \quad \checkmark$$

$$c_8 : A_1 \vee A_8 \quad \checkmark$$

$$c_9 : \neg A_7 \vee \neg A_8 \vee \neg A_{13}$$

...



$\{\dots, \neg A_9, \neg A_{10}, \neg A_{11}, A_{12}, A_{13}, \dots, A_1\}$

... (branch on A_1)

Classic chronological backtracking – example (4)

$$c_1 : \neg A_1 \vee A_2 \quad \checkmark$$

$$c_2 : \neg A_1 \vee A_3 \vee A_9 \quad \checkmark$$

$$c_3 : \neg A_2 \vee \neg A_3 \vee A_4$$

$$c_4 : \neg A_4 \vee A_5 \vee A_{10}$$

$$c_5 : \neg A_4 \vee A_6 \vee A_{11}$$

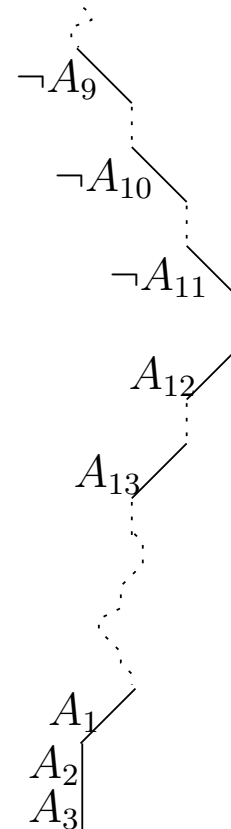
$$c_6 : \neg A_5 \vee \neg A_6$$

$$c_7 : A_1 \vee A_7 \vee \neg A_{12} \quad \checkmark$$

$$c_8 : A_1 \vee A_8 \quad \checkmark$$

$$c_9 : \neg A_7 \vee \neg A_8 \vee \neg A_{13}$$

...



$\{\dots, \neg A_9, \neg A_{10}, \neg A_{11}, A_{12}, A_{13}, \dots, A_1, A_2, A_3\}$

(unit A_2, A_3)

Classic chronological backtracking – example (5)

$$c_1 : \neg A_1 \vee A_2 \quad \checkmark$$

$$c_2 : \neg A_1 \vee A_3 \vee A_9 \quad \checkmark$$

$$c_3 : \neg A_2 \vee \neg A_3 \vee A_4 \quad \checkmark$$

$$c_4 : \neg A_4 \vee A_5 \vee A_{10}$$

$$c_5 : \neg A_4 \vee A_6 \vee A_{11}$$

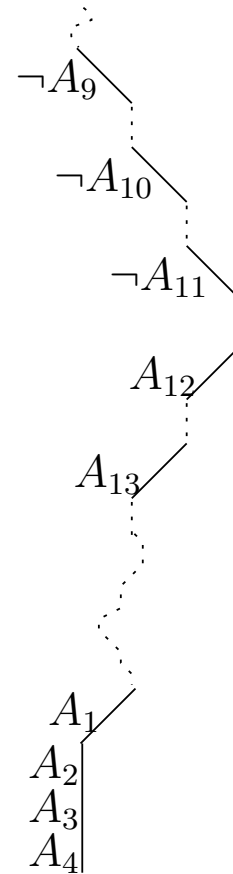
$$c_6 : \neg A_5 \vee \neg A_6$$

$$c_7 : A_1 \vee A_7 \vee \neg A_{12} \quad \checkmark$$

$$c_8 : A_1 \vee A_8 \quad \checkmark$$

$$c_9 : \neg A_7 \vee \neg A_8 \vee \neg A_{13}$$

...



$\{\dots, \neg A_9, \neg A_{10}, \neg A_{11}, A_{12}, A_{13}, \dots, A_1, A_2, A_3, A_4\}$

(unit A_4)

Classic chronological backtracking – example (6)

$$c_1 : \neg A_1 \vee A_2 \quad \checkmark$$

$$c_2 : \neg A_1 \vee A_3 \vee A_9 \quad \checkmark$$

$$c_3 : \neg A_2 \vee \neg A_3 \vee A_4 \quad \checkmark$$

$$c_4 : \neg A_4 \vee A_5 \vee A_{10} \quad \checkmark$$

$$c_5 : \neg A_4 \vee A_6 \vee A_{11} \quad \checkmark$$

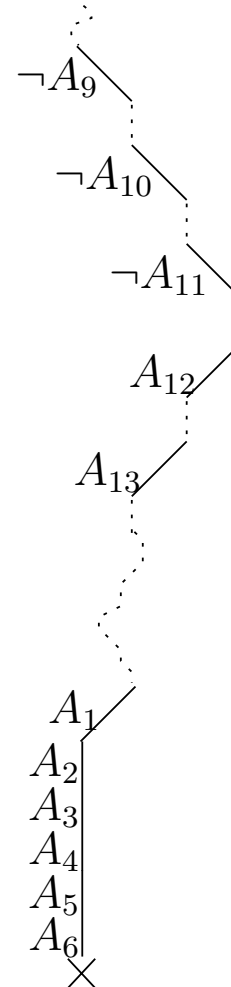
$$c_6 : \neg A_5 \vee \neg A_6 \quad \times$$

$$c_7 : A_1 \vee A_7 \vee \neg A_{12} \quad \checkmark$$

$$c_8 : A_1 \vee A_8 \quad \checkmark$$

$$c_9 : \neg A_7 \vee \neg A_8 \vee \neg A_{13}$$

...



$\{\dots, \neg A_9, \neg A_{10}, \neg A_{11}, A_{12}, A_{13}, \dots, A_1, A_2, A_3, A_4, A_5, A_6\}$

(unit A_5, A_6) \implies conflict

Classic chronological backtracking – example (7)

$$c_1 : \neg A_1 \vee A_2$$

$$c_2 : \neg A_1 \vee A_3 \vee A_9$$

$$c_3 : \neg A_2 \vee \neg A_3 \vee A_4$$

$$c_4 : \neg A_4 \vee A_5 \vee A_{10}$$

$$c_5 : \neg A_4 \vee A_6 \vee A_{11}$$

$$c_6 : \neg A_5 \vee \neg A_6$$

$$c_7 : A_1 \vee A_7 \vee \neg A_{12}$$

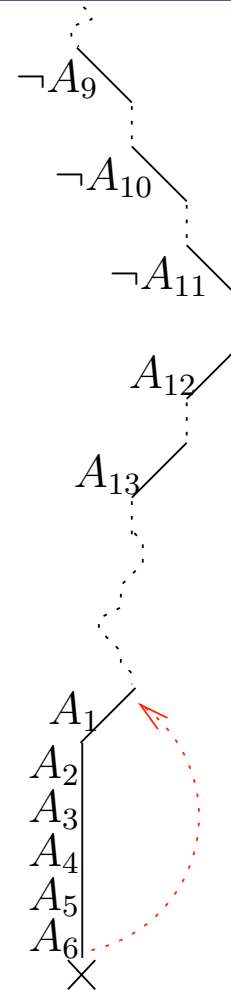
$$c_8 : A_1 \vee A_8$$

$$c_9 : \neg A_7 \vee \neg A_8 \vee \neg A_{13}$$

...

$\{\dots, \neg A_9, \neg A_{10}, \neg A_{11}, A_{12}, A_{13}, \dots\}$

\implies backtrack up to A_1



Classic chronological backtracking – example (8)

$$c_1 : \neg A_1 \vee A_2 \quad \checkmark$$

$$c_2 : \neg A_1 \vee A_3 \vee A_9 \quad \checkmark$$

$$c_3 : \neg A_2 \vee \neg A_3 \vee A_4$$

$$c_4 : \neg A_4 \vee A_5 \vee A_{10}$$

$$c_5 : \neg A_4 \vee A_6 \vee A_{11}$$

$$c_6 : \neg A_5 \vee \neg A_6$$

$$c_7 : A_1 \vee A_7 \vee \neg A_{12}$$

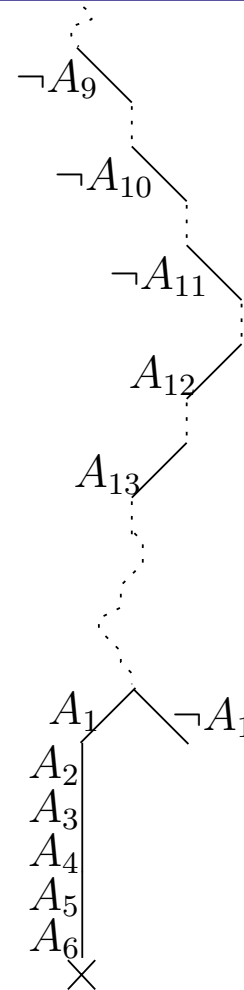
$$c_8 : A_1 \vee A_8$$

$$c_9 : \neg A_7 \vee \neg A_8 \vee \neg A_{13}$$

...

$$\{\dots, \neg A_9, \neg A_{10}, \neg A_{11}, A_{12}, A_{13}, \dots, \neg A_1\}$$

(unit $\neg A_1$)



Classic chronological backtracking – example (9)

$$c_1 : \neg A_1 \vee A_2 \quad \checkmark$$

$$c_2 : \neg A_1 \vee A_3 \vee A_9 \quad \checkmark$$

$$c_3 : \neg A_2 \vee \neg A_3 \vee A_4$$

$$c_4 : \neg A_4 \vee A_5 \vee A_{10}$$

$$c_5 : \neg A_4 \vee A_6 \vee A_{11}$$

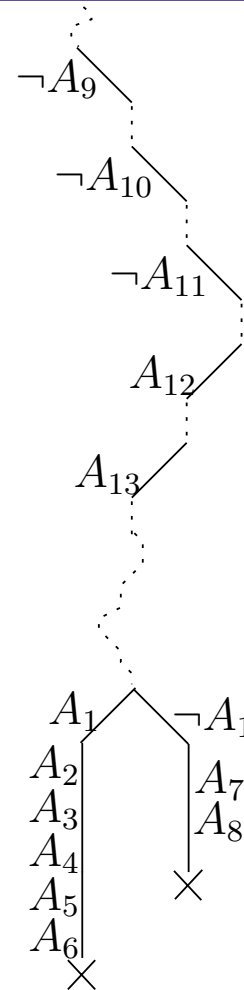
$$c_6 : \neg A_5 \vee \neg A_6$$

$$c_7 : A_1 \vee A_7 \vee \neg A_{12} \quad \checkmark$$

$$c_8 : A_1 \vee A_8 \quad \checkmark$$

$$c_9 : \neg A_7 \vee \neg A_8 \vee \neg A_{13} \quad \times$$

...



$\{\dots, \neg A_9, \neg A_{10}, \neg A_{11}, A_{12}, A_{13}, \dots, \neg A_1, A_7, A_8\}$

(unit A_7, A_8) \implies conflict

Classic chronological backtracking – example (10)

$$c_1 : \neg A_1 \vee A_2$$

$$c_2 : \neg A_1 \vee A_3 \vee A_9$$

$$c_3 : \neg A_2 \vee \neg A_3 \vee A_4$$

$$c_4 : \neg A_4 \vee A_5 \vee A_{10}$$

$$c_5 : \neg A_4 \vee A_6 \vee A_{11}$$

$$c_6 : \neg A_5 \vee \neg A_6$$

$$c_7 : A_1 \vee A_7 \vee \neg A_{12}$$

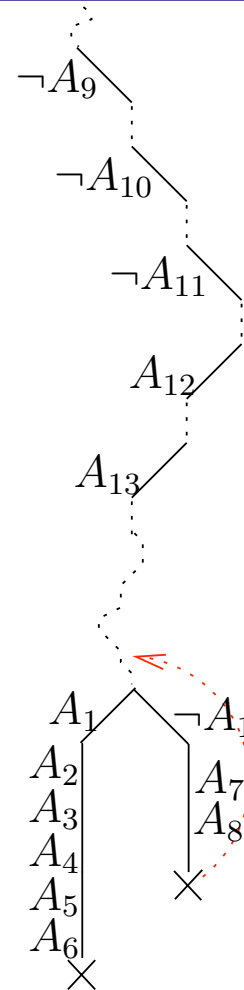
$$c_8 : A_1 \vee A_8$$

$$c_9 : \neg A_7 \vee \neg A_8 \vee \neg A_{13}$$

...

$$\{\dots, \neg A_9, \neg A_{10}, \neg A_{11}, A_{12}, A_{13}, \dots\}$$

\implies backtrack to the most recent open branching point



Classic chronological backtracking – example (10)

$$c_1 : \neg A_1 \vee A_2$$

$$c_2 : \neg A_1 \vee A_3 \vee A_9$$

$$c_3 : \neg A_2 \vee \neg A_3 \vee A_4$$

$$c_4 : \neg A_4 \vee A_5 \vee A_{10}$$

$$c_5 : \neg A_4 \vee A_6 \vee A_{11}$$

$$c_6 : \neg A_5 \vee \neg A_6$$

$$c_7 : A_1 \vee A_7 \vee \neg A_{12}$$

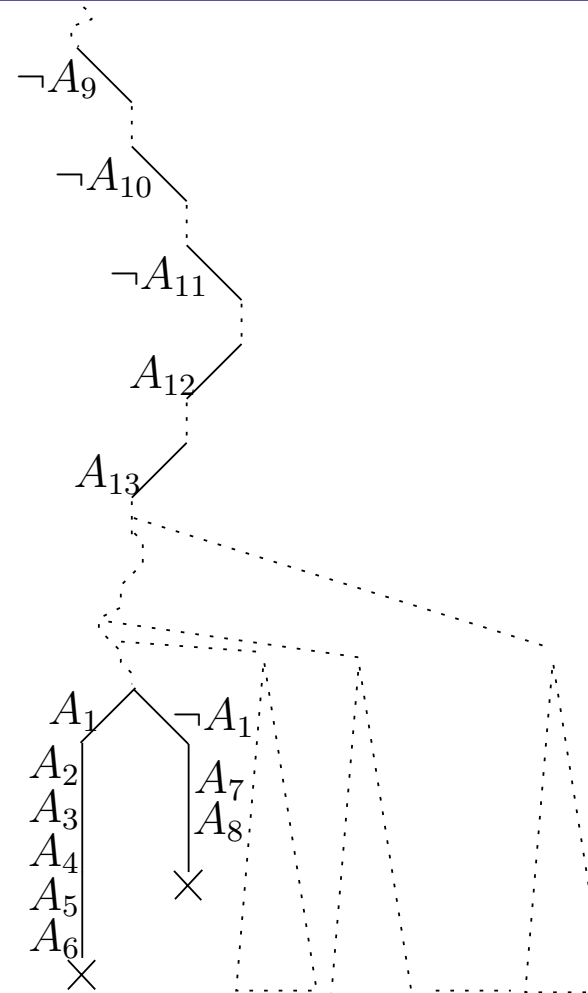
$$c_8 : A_1 \vee A_8$$

$$c_9 : \neg A_7 \vee \neg A_8 \vee \neg A_{13}$$

...

$$\{\dots, \neg A_9, \neg A_{10}, \neg A_{11}, A_{12}, A_{13}, \dots\}$$

⇒ lots of useless search before backtracking up to A_{13} !



Classic chronological backtracking: drawbacks

- ▷ often the branch heuristic delays the “right” choice
- ▷ chronological backtracking always backtracks to the most recent branching point, even though a higher backtrack could be possible
⇒ lots of useless search!

Conflict-directed backtracking (backjumping) and learning [4, 46]

- ▷ **General idea:** when a branch μ fails,
 1. **conflict analysis:** reveal the sub-assignment $\eta \subseteq \mu$ causing the failure (**conflict set η**)
 2. **learning:** add the **conflict clause** $C \stackrel{\text{def}}{=} \neg\eta$ to the clause set
 3. **backjumping:** use η to decide the point where to backtrack
- ▷ may jump back up much more than one decision level in the stack
 \implies **may avoid lots of redundant search!!**.
- ▷ we illustrate two main backjumping & learning strategies:
 - the original strategy presented in [46]
 - the state-of-the-art 1stUIP strategy [54]

Preliminary: Correspondence between Search trees and Resolution Proofs

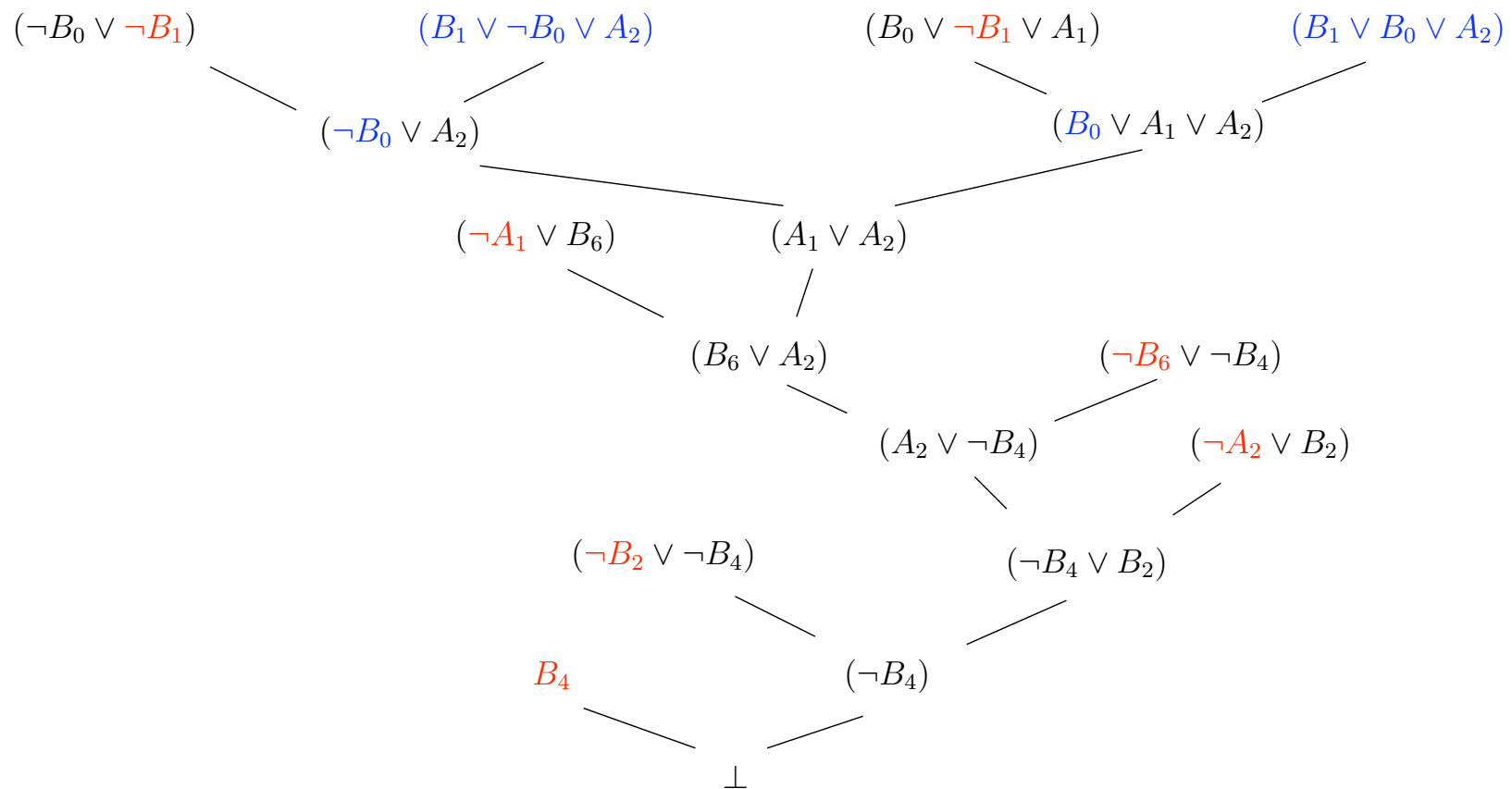
In the case of an unsatisfiable formula, the search tree explored by DPLL corresponds to a (tree) resolution proof of its unsatisfiability.

Given the above, “learning” corresponds to storing intermediate resolution steps computed during the search.

Example

$$(B_0 \vee \neg B_1 \vee A_1) \wedge (B_0 \vee B_1 \vee A_2) \wedge (\neg B_0 \vee B_1 \vee A_2) \wedge (\neg B_0 \vee \neg B_1) \wedge (\neg B_2 \vee \neg B_4) \wedge$$

$$(\neg A_2 \vee B_2) \wedge (\neg A_1 \vee B_3) \wedge B_4 \wedge (A_2 \vee B_5) \wedge (\neg B_6 \vee \neg B_4) \wedge (B_6 \vee \neg A_1) \wedge B_7$$



The original backjumping and learning strategy of [46]

- ▷ **Idea:** when a branch μ fails,
1. **conflict analysis:** find the conflict set $\eta \subseteq \mu$ by generating the conflict clause $C \stackrel{\text{def}}{=} \neg\eta$ via resolution from the falsified clause (conflicting clause)
 2. **learning:** add the conflict clause C to the clause set
 3. **backjumping:** backtrack to the most recent branching point s.t. the stack does not fully contain η , and then unit-propagate the unassigned literal on C

Construction of a conflict set: implication graph

- ▷ An **implication graph** is a DAG s.t.:
 - each node represents a variable assignment (literal)
 - each edge $l_i \xrightarrow{c} l$ is labeled with a clause
 - the node of a decision literal has no incoming edges
 - all edges incoming into a node l are labeled with the same clause c , s.t. $l_1 \xrightarrow{c} l, \dots, l_n \xrightarrow{c} l$ iff $c = \neg l_1 \vee \dots \vee \neg l_n \vee l$ (c is said to be the **antecedent clause** of l)
 - when both l and $\neg l$ occur in the graph, we have a **conflict**.
- ▷ **Intuition:**
 - the graph contains $l_1 \xrightarrow{c} l, \dots, l_n \xrightarrow{c} l$ iff l has been obtained from l_1, \dots, l_n by unit propagation on c
 - a partition of the graph with all decision literals on one side and the conflict on the other represents a **conflict set**

The original backjumping strategy – example [46] (1)

$$c_1 : \neg A_1 \vee A_2$$

$$c_2 : \neg A_1 \vee A_3 \vee A_9$$

$$c_3 : \neg A_2 \vee \neg A_3 \vee A_4$$

$$c_4 : \neg A_4 \vee A_5 \vee A_{10}$$

$$c_5 : \neg A_4 \vee A_6 \vee A_{11}$$

$$c_6 : \neg A_5 \vee \neg A_6$$

$$c_7 : A_1 \vee A_7 \vee \neg A_{12}$$

$$c_8 : A_1 \vee A_8$$

$$c_9 : \neg A_7 \vee \neg A_8 \vee \neg A_{13}$$

...

The original backjumping strategy – example (2)

$$c_1 : \neg A_1 \vee A_2$$

$$c_2 : \neg A_1 \vee A_3 \vee A_9$$

$$c_3 : \neg A_2 \vee \neg A_3 \vee A_4$$

$$c_4 : \neg A_4 \vee A_5 \vee A_{10}$$

$$c_5 : \neg A_4 \vee A_6 \vee A_{11}$$

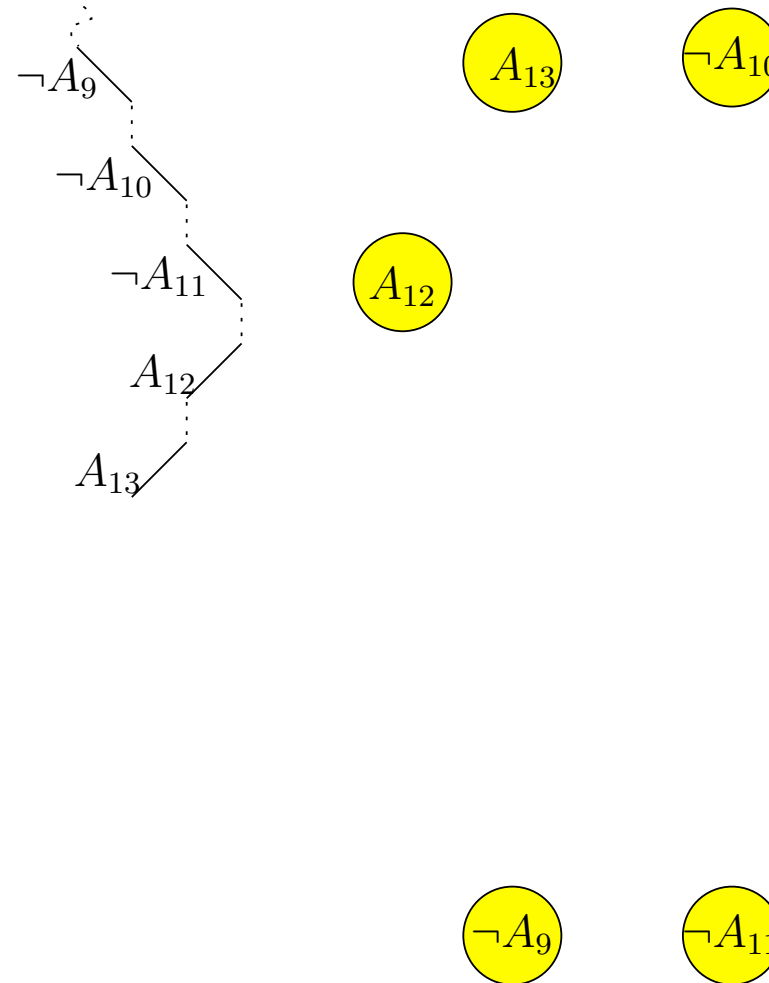
$$c_6 : \neg A_5 \vee \neg A_6$$

$$c_7 : A_1 \vee A_7 \vee \neg A_{12}$$

$$c_8 : A_1 \vee A_8$$

$$c_9 : \neg A_7 \vee \neg A_8 \vee \neg A_{13}$$

...



$\{\dots, \neg A_9, \neg A_{10}, \neg A_{11}, A_{12}, A_{13}, \dots\}$

(initial assignment)

The original backjumping strategy – example (3)

$$c_1 : \neg A_1 \vee A_2$$

$$c_2 : \neg A_1 \vee A_3 \vee A_9$$

$$c_3 : \neg A_2 \vee \neg A_3 \vee A_4$$

$$c_4 : \neg A_4 \vee A_5 \vee A_{10}$$

$$c_5 : \neg A_4 \vee A_6 \vee A_{11}$$

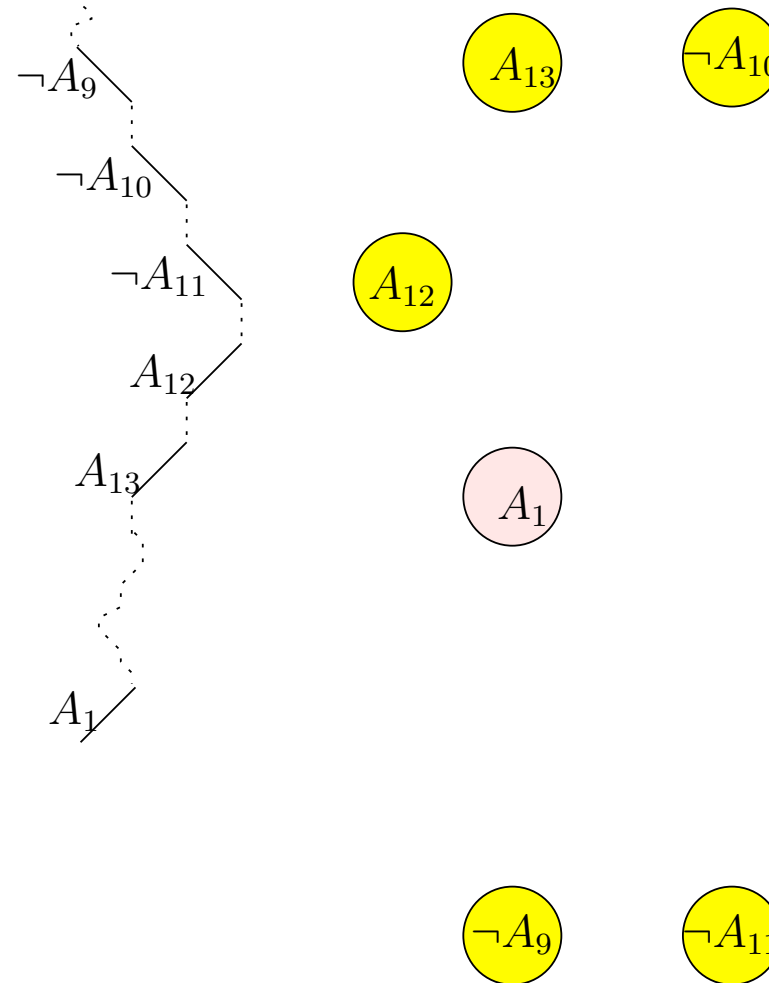
$$c_6 : \neg A_5 \vee \neg A_6$$

$$c_7 : A_1 \vee A_7 \vee \neg A_{12} \quad \checkmark$$

$$c_8 : A_1 \vee A_8 \quad \checkmark$$

$$c_9 : \neg A_7 \vee \neg A_8 \vee \neg A_{13}$$

...



$\{\dots, \neg A_9, \neg A_{10}, \neg A_{11}, A_{12}, A_{13}, \dots, A_1\}$

... (branch on A_1)

The original backjumping strategy – example (4)

$$c_1 : \neg A_1 \vee A_2 \quad \checkmark$$

$$c_2 : \neg A_1 \vee A_3 \vee A_9 \quad \checkmark$$

$$c_3 : \neg A_2 \vee \neg A_3 \vee A_4$$

$$c_4 : \neg A_4 \vee A_5 \vee A_{10}$$

$$c_5 : \neg A_4 \vee A_6 \vee A_{11}$$

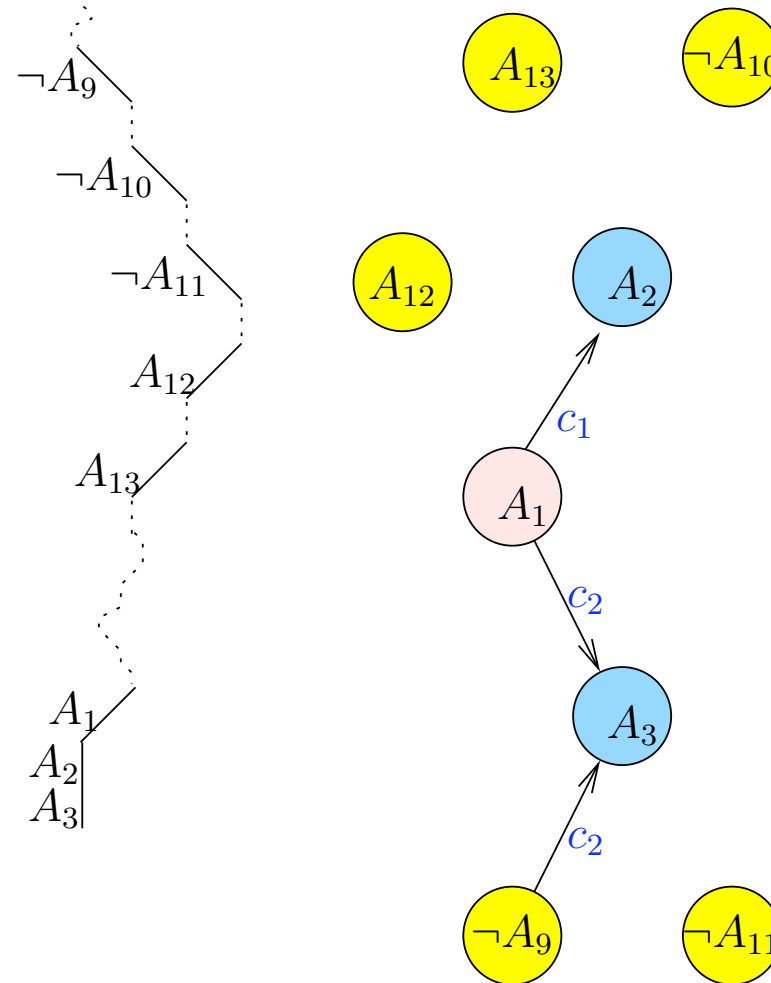
$$c_6 : \neg A_5 \vee \neg A_6$$

$$c_7 : A_1 \vee A_7 \vee \neg A_{12} \quad \checkmark$$

$$c_8 : A_1 \vee A_8 \quad \checkmark$$

$$c_9 : \neg A_7 \vee \neg A_8 \vee \neg A_{13}$$

...



$\{\dots, \neg A_9, \neg A_{10}, \neg A_{11}, A_{12}, A_{13}, \dots, A_1, A_2, A_3\}$

(unit A_2, A_3)

The original backjumping strategy – example (5)

$$c_1 : \neg A_1 \vee A_2 \quad \checkmark$$

$$c_2 : \neg A_1 \vee A_3 \vee A_9 \quad \checkmark$$

$$c_3 : \neg A_2 \vee \neg A_3 \vee A_4 \quad \checkmark$$

$$c_4 : \neg A_4 \vee A_5 \vee A_{10}$$

$$c_5 : \neg A_4 \vee A_6 \vee A_{11}$$

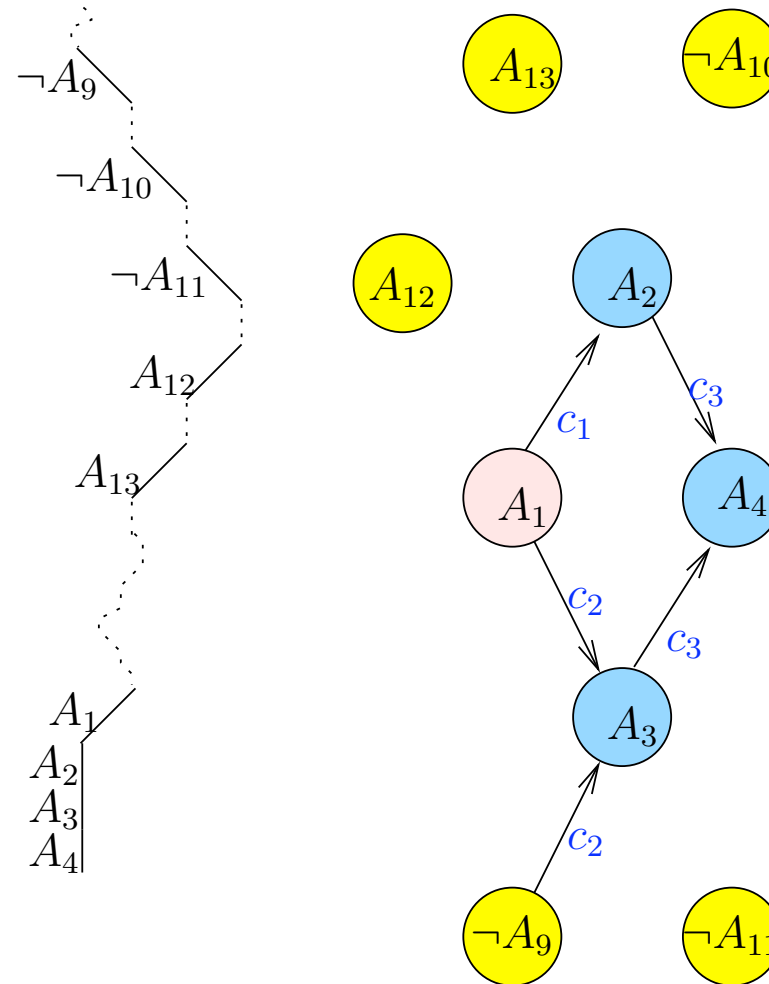
$$c_6 : \neg A_5 \vee \neg A_6$$

$$c_7 : A_1 \vee A_7 \vee \neg A_{12} \quad \checkmark$$

$$c_8 : A_1 \vee A_8 \quad \checkmark$$

$$c_9 : \neg A_7 \vee \neg A_8 \vee \neg A_{13}$$

...

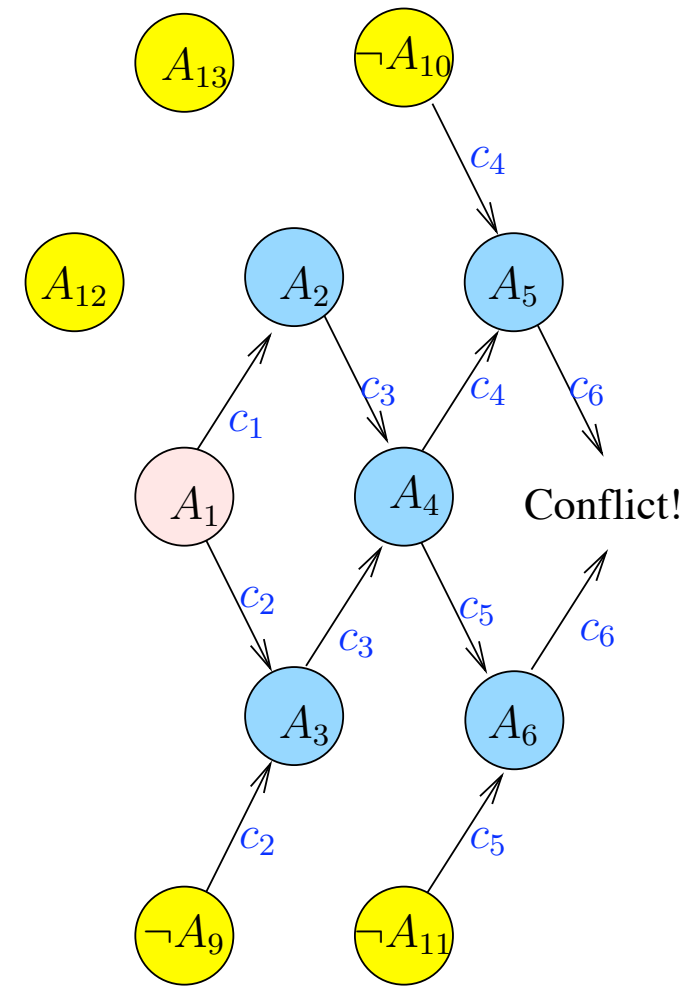
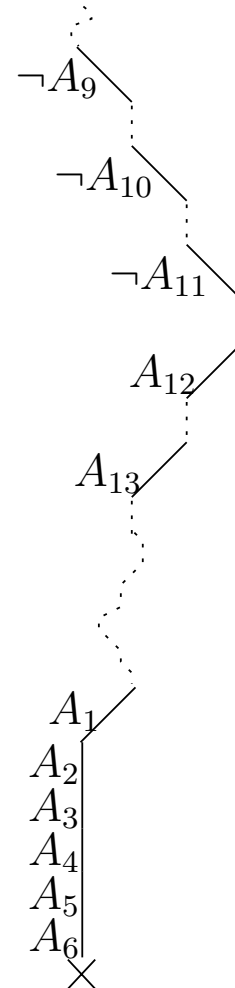


$\{\dots, \neg A_9, \neg A_{10}, \neg A_{11}, A_{12}, A_{13}, \dots, A_1, A_2, A_3, A_4\}$

(unit A_4)

The original backjumping strategy – example (6)

- $c_1 : \neg A_1 \vee A_2$ ✓
- $c_2 : \neg A_1 \vee A_3 \vee A_9$ ✓
- $c_3 : \neg A_2 \vee \neg A_3 \vee A_4$ ✓
- $c_4 : \neg A_4 \vee A_5 \vee A_{10}$ ✓
- $c_5 : \neg A_4 \vee A_6 \vee A_{11}$ ✓
- $c_6 : \neg A_5 \vee \neg A_6$ ✗
- $c_7 : A_1 \vee A_7 \vee \neg A_{12}$ ✓
- $c_8 : A_1 \vee A_8$ ✓
- $c_9 : \neg A_7 \vee \neg A_8 \vee \neg A_{13}$
- ...



$\{\dots, \neg A_9, \neg A_{10}, \neg A_{11}, A_{12}, A_{13}, \dots, A_1, A_2, A_3, A_4, A_5, A_6\}$
 (unit A_5, A_6) \implies **conflict**

The original backjumping strategy – example (7)

$$c_1 : \neg A_1 \vee A_2 \quad \checkmark$$

$$c_2 : \neg A_1 \vee A_3 \vee A_9 \quad \checkmark$$

$$c_3 : \neg A_2 \vee \neg A_3 \vee A_4 \quad \checkmark$$

$$c_4 : \neg A_4 \vee A_5 \vee A_{10} \quad \checkmark$$

$$c_5 : \neg A_4 \vee A_6 \vee A_{11} \quad \checkmark$$

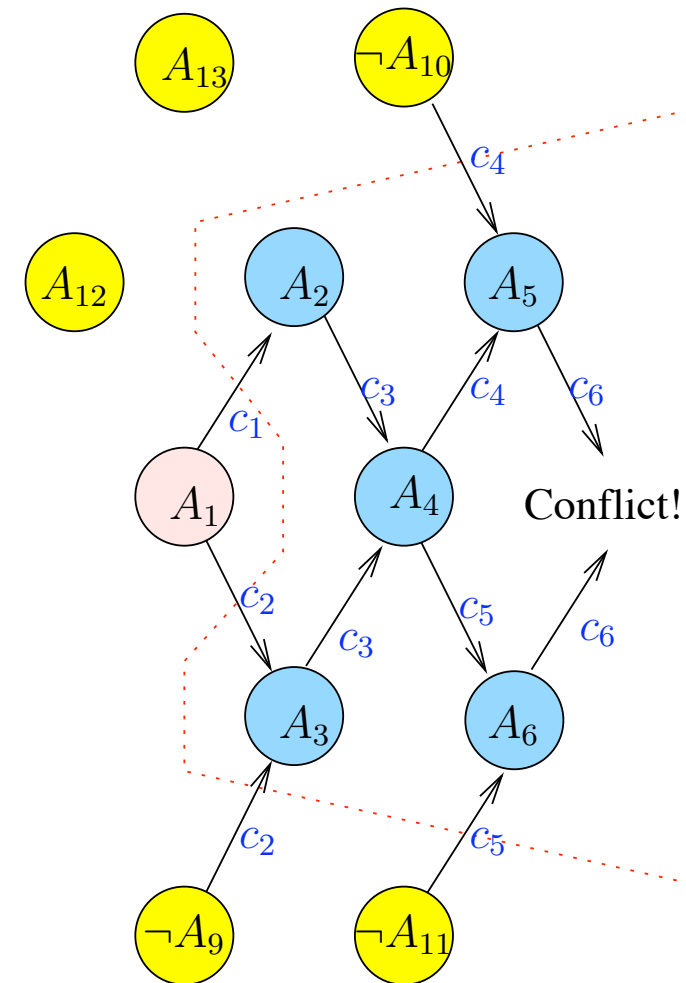
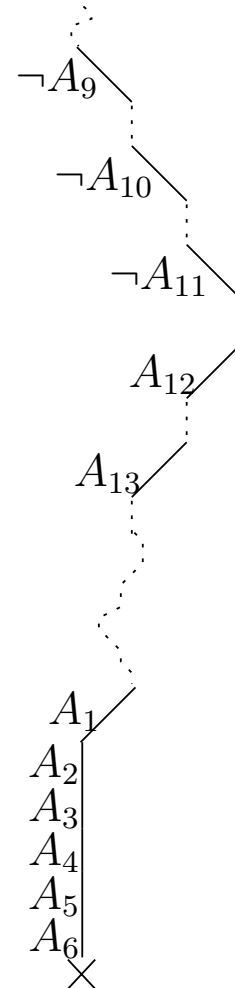
$$c_6 : \neg A_5 \vee \neg A_6 \quad \times$$

$$c_7 : A_1 \vee A_7 \vee \neg A_{12} \quad \checkmark$$

$$c_8 : A_1 \vee A_8 \quad \checkmark$$

$$c_9 : \neg A_7 \vee \neg A_8 \vee \neg A_{13}$$

...



⇒ Conflict set: $\{\neg A_9, \neg A_{10}, \neg A_{11}, A_1\}$

⇒ learn the conflict clause $c_{10} := A_9 \vee A_{10} \vee A_{11} \vee \neg A_1$

Implementation of the implication graph

- ▷ an implication graph is implemented by tagging each non-decision literal in the stack with its antecedent clause
- ▷ (the partition representing) a **conflict set** is constructed from the conflict by traversing backwards the implication graph
- ▷ a **conflict set** can be constructed starting from the conflicting clause, each time resolving the current clause with the antecedent clause of one of its literals l
(undo the unit propagation of l)

Building a conflict set/clause by resolution

1. $C :=$ conflicting clause
 2. repeat
 - (a) resolve current clause C with the antecedent clause of the last unit-propagated literal l in C
- until C verifies some given termination criteria
(e.g., until C contains only decision literals)

$$\begin{array}{c}
 \frac{\neg A_1 \vee A_2}{\neg A_1 \vee A_3 \vee A_9} \quad \frac{\neg A_2 \vee \neg A_3 \vee A_4}{\neg A_2 \vee \neg A_3 \vee A_{10} \vee A_{11}} \quad (A_2) \\
 \frac{\neg A_1 \vee A_3 \vee A_9 \quad \neg A_2 \vee \neg A_3 \vee A_{10} \vee A_{11}}{\neg A_1 \vee A_9 \vee A_{10} \vee A_{11}} \quad (A_3) \\
 \frac{\neg A_2 \vee \neg A_3 \vee A_{10} \quad \neg A_4 \vee A_5 \vee A_{10}}{\neg A_2 \vee \neg A_3 \vee A_{10} \vee A_{11}} \quad (A_4) \\
 \frac{\neg A_4 \vee A_5 \vee A_{10} \quad \neg A_4 \vee A_6 \vee A_{11}}{\neg A_4 \vee \neg A_5 \vee A_{11}} \quad (A_5) \\
 \frac{\neg A_4 \vee \neg A_5 \vee A_{11} \quad \overbrace{\neg A_5 \vee \neg A_6}^{\text{Conflicting cl.}}}{\neg A_4 \vee A_6 \vee A_{11}} \quad (A_6)
 \end{array}$$

The original backjumping strategy – example (7)

$$c_1 : \neg A_1 \vee A_2 \quad \checkmark$$

$$c_2 : \neg A_1 \vee A_3 \vee A_9 \quad \checkmark$$

$$c_3 : \neg A_2 \vee \neg A_3 \vee A_4 \quad \checkmark$$

$$c_4 : \neg A_4 \vee A_5 \vee A_{10} \quad \checkmark$$

$$c_5 : \neg A_4 \vee A_6 \vee A_{11} \quad \checkmark$$

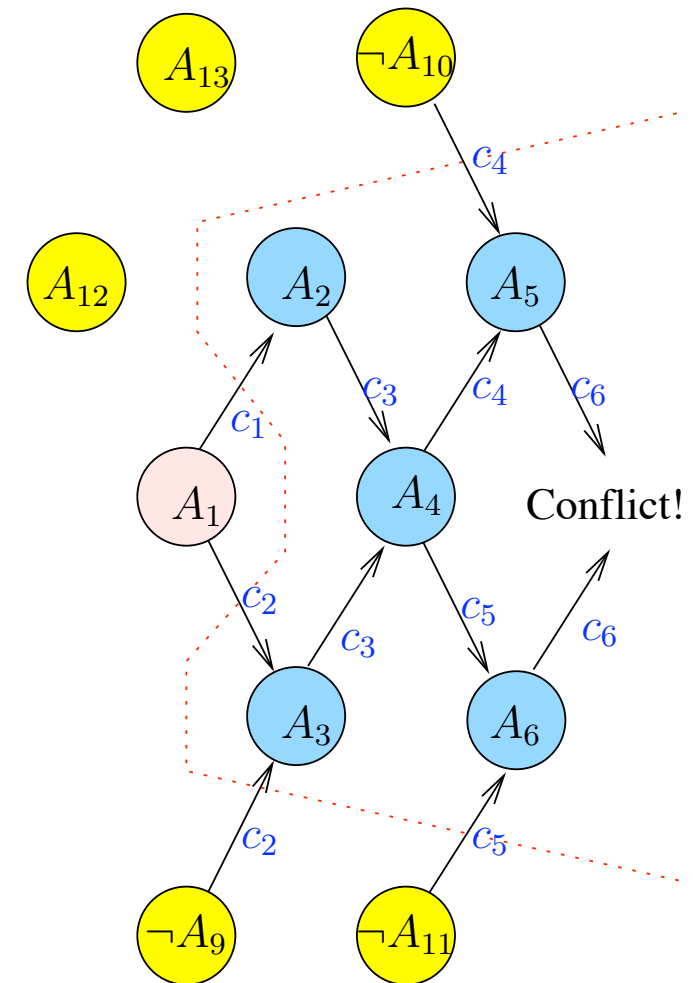
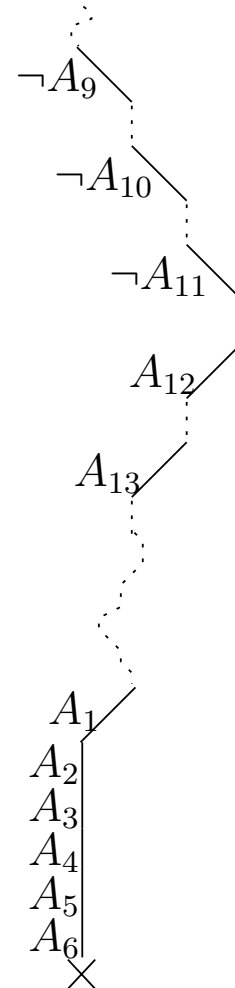
$$c_6 : \neg A_5 \vee \neg A_6 \quad \times$$

$$c_7 : A_1 \vee A_7 \vee \neg A_{12} \quad \checkmark$$

$$c_8 : A_1 \vee A_8 \quad \checkmark$$

$$c_9 : \neg A_7 \vee \neg A_8 \vee \neg A_{13}$$

...



\implies Conflict set: $\{\neg A_9, \neg A_{10}, \neg A_{11}, A_1\}$

\implies learn the conflict clause $c_{10} := A_9 \vee A_{10} \vee A_{11} \vee \neg A_1$

The original backjumping strategy – example (8)

$$c_1 : \neg A_1 \vee A_2$$

$$c_2 : \neg A_1 \vee A_3 \vee A_9$$

$$c_3 : \neg A_2 \vee \neg A_3 \vee A_4$$

$$c_4 : \neg A_4 \vee A_5 \vee A_{10}$$

$$c_5 : \neg A_4 \vee A_6 \vee A_{11}$$

$$c_6 : \neg A_5 \vee \neg A_6$$

$$c_7 : A_1 \vee A_7 \vee \neg A_{12}$$

$$c_8 : A_1 \vee A_8$$

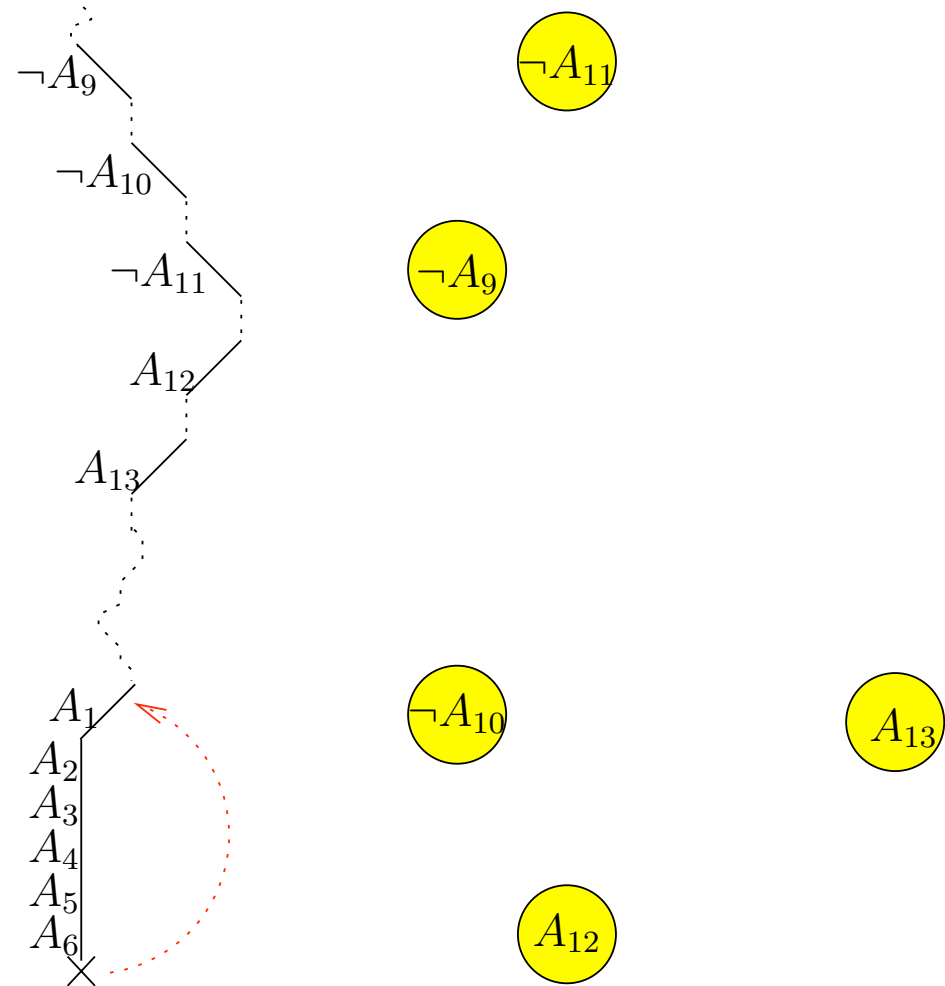
$$c_9 : \neg A_7 \vee \neg A_8 \vee \neg A_{13}$$

$$c_{10} : A_9 \vee A_{10} \vee A_{11} \vee \neg A_1$$

...

$$\{\dots, \neg A_9, \neg A_{10}, \neg A_{11}, A_{12}, A_{13}, \dots\}$$

\implies backtrack up to A_1



The original backjumping strategy – example (9)

$$c_1 : \neg A_1 \vee A_2 \quad \checkmark$$

$$c_2 : \neg A_1 \vee A_3 \vee A_9 \quad \checkmark$$

$$c_3 : \neg A_2 \vee \neg A_3 \vee A_4$$

$$c_4 : \neg A_4 \vee A_5 \vee A_{10}$$

$$c_5 : \neg A_4 \vee A_6 \vee A_{11}$$

$$c_6 : \neg A_5 \vee \neg A_6$$

$$c_7 : A_1 \vee A_7 \vee \neg A_{12}$$

$$c_8 : A_1 \vee A_8$$

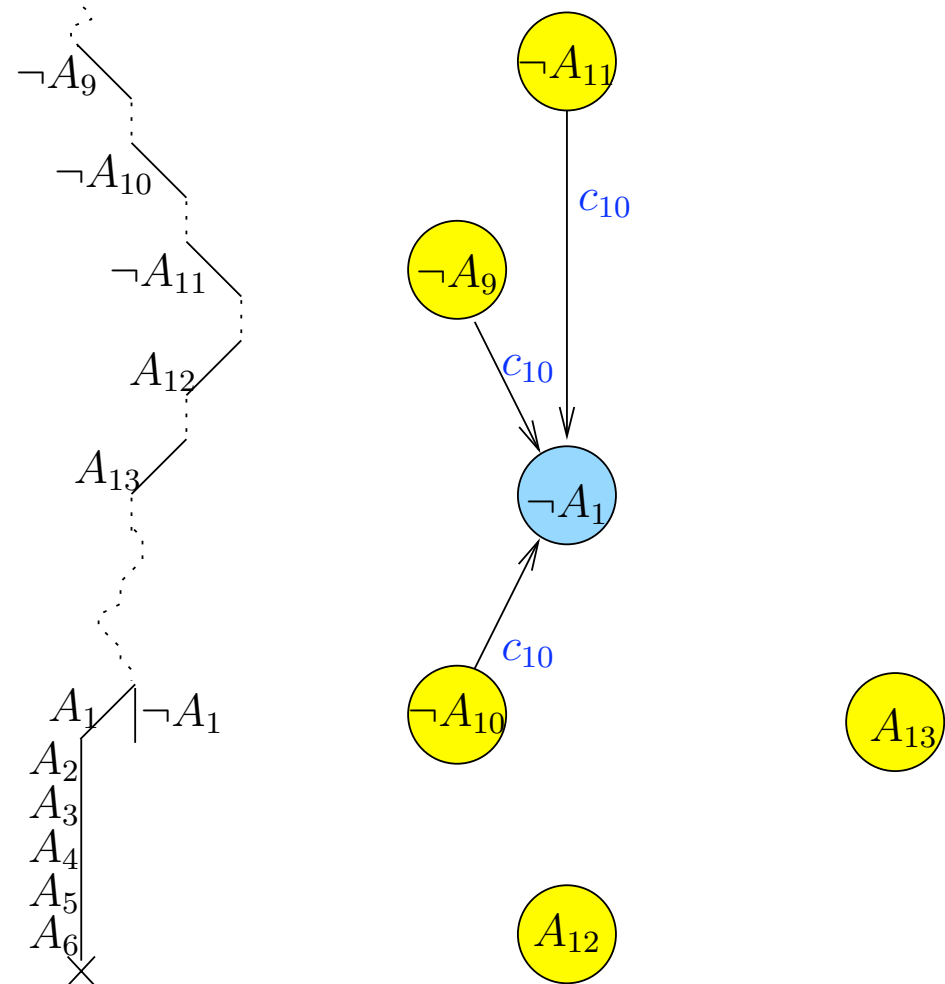
$$c_9 : \neg A_7 \vee \neg A_8 \vee \neg A_{13}$$

$$c_{10} : A_9 \vee A_{10} \vee A_{11} \vee \neg A_1 \quad \checkmark$$

...

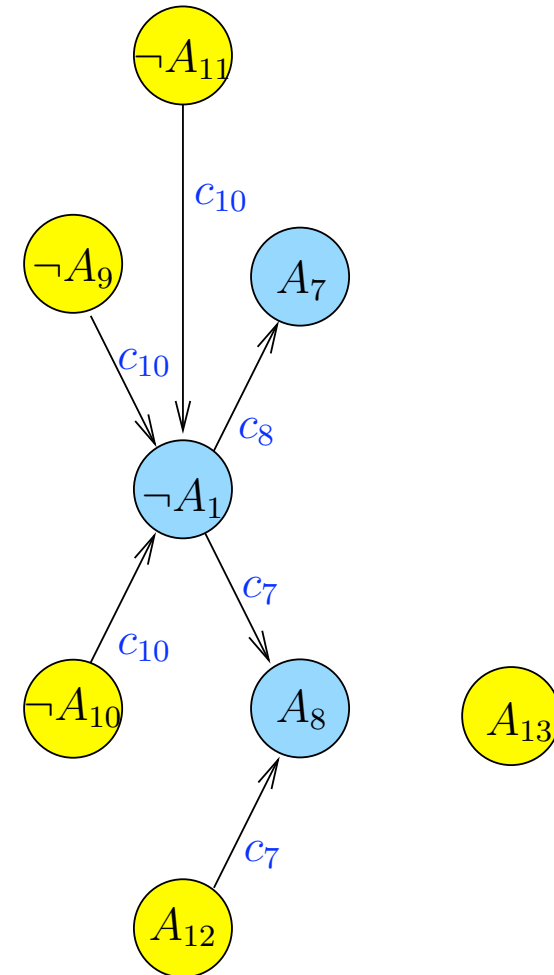
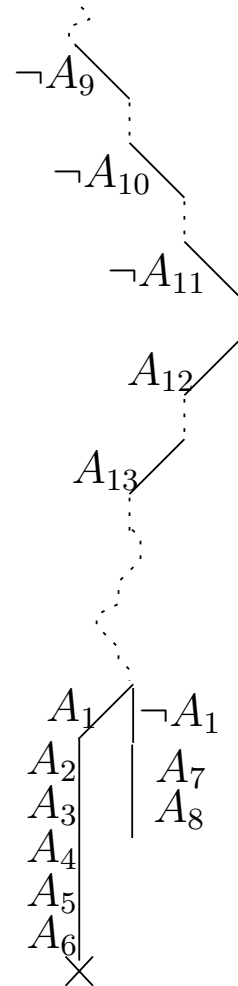
$$\{\dots, \neg A_9, \neg A_{10}, \neg A_{11}, A_{12}, A_{13}, \dots, \neg A_1\}$$

(unit $\neg A_1$)



The original backjumping strategy – example (10)

- $c_1 : \neg A_1 \vee A_2$ ✓
- $c_2 : \neg A_1 \vee A_3 \vee A_9$ ✓
- $c_3 : \neg A_2 \vee \neg A_3 \vee A_4$
- $c_4 : \neg A_4 \vee A_5 \vee A_{10}$
- $c_5 : \neg A_4 \vee A_6 \vee A_{11}$
- $c_6 : \neg A_5 \vee \neg A_6$
- $c_7 : A_1 \vee A_7 \vee \neg A_{12}$ ✓
- $c_8 : A_1 \vee A_8$ ✓
- $c_9 : \neg A_7 \vee \neg A_8 \vee \neg A_{13}$
- $c_{10} : A_9 \vee A_{10} \vee A_{11} \vee \neg A_1$ ✓
- ...



$\{\dots, \neg A_9, \neg A_{10}, \neg A_{11}, A_{12}, A_{13}, \dots, \neg A_1, A_7, A_8\}$
 (unit A_7, A_8)

The original backjumping strategy – example (11)

$$c_1 : \neg A_1 \vee A_2 \quad \checkmark$$

$$c_2 : \neg A_1 \vee A_3 \vee A_9 \quad \checkmark$$

$$c_3 : \neg A_2 \vee \neg A_3 \vee A_4$$

$$c_4 : \neg A_4 \vee A_5 \vee A_{10}$$

$$c_5 : \neg A_4 \vee A_6 \vee A_{11}$$

$$c_6 : \neg A_5 \vee \neg A_6$$

$$c_7 : A_1 \vee A_7 \vee \neg A_{12} \quad \checkmark$$

$$c_8 : A_1 \vee A_8 \quad \checkmark$$

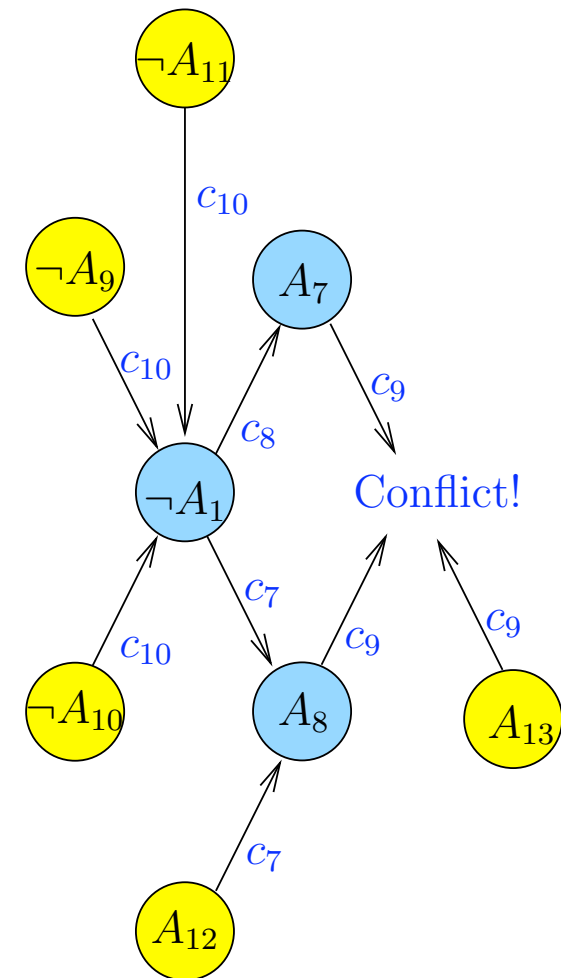
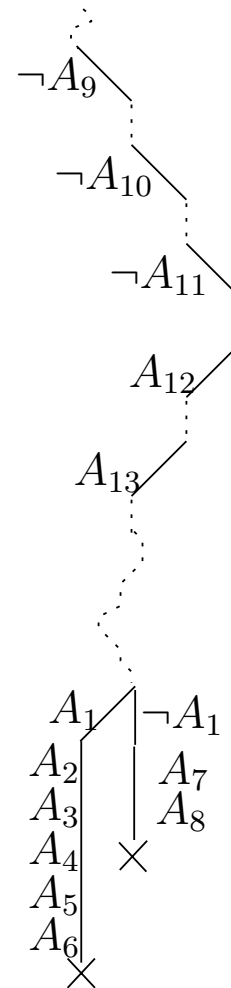
$$c_9 : \neg A_7 \vee \neg A_8 \vee \neg A_{13} \quad \times$$

$$c_{10} : A_9 \vee A_{10} \vee A_{11} \vee \neg A_1 \quad \checkmark$$

...

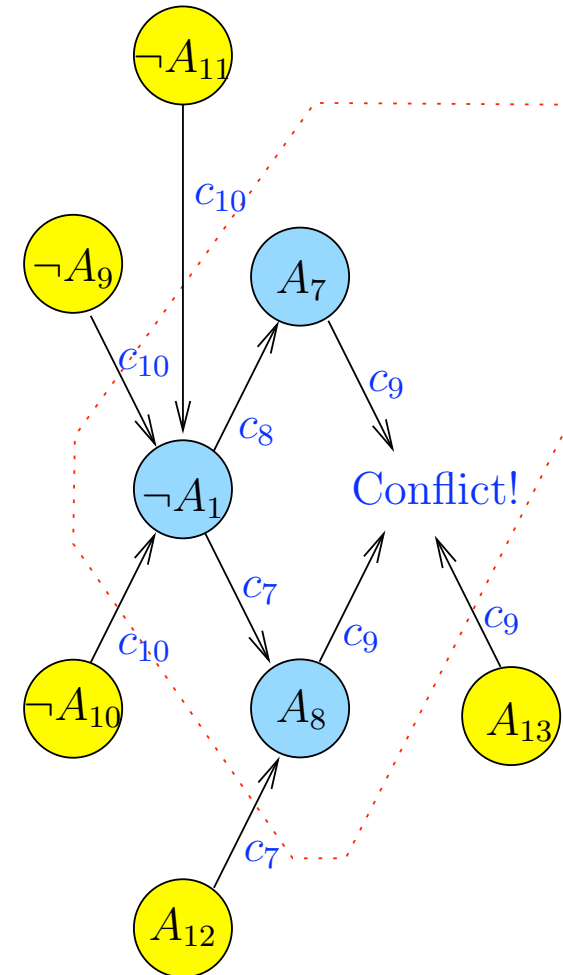
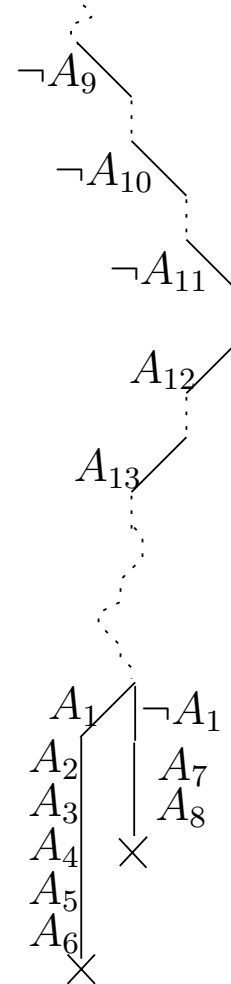
{..., $\neg A_9$, $\neg A_{10}$, $\neg A_{11}$, A_{12} , A_{13} , ..., $\neg A_1$, A_7 , A_8 }

Conflict!



The original backjumping strategy – example (12)

- $c_1 : \neg A_1 \vee A_2$ ✓
- $c_2 : \neg A_1 \vee A_3 \vee A_9$ ✓
- $c_3 : \neg A_2 \vee \neg A_3 \vee A_4$
- $c_4 : \neg A_4 \vee A_5 \vee A_{10}$
- $c_5 : \neg A_4 \vee A_6 \vee A_{11}$
- $c_6 : \neg A_5 \vee \neg A_6$
- $c_7 : A_1 \vee A_7 \vee \neg A_{12}$ ✓
- $c_8 : A_1 \vee A_8$ ✓
- $c_9 : \neg A_7 \vee \neg A_8 \vee \neg A_{13}$ ✗
- $c_{10} : A_9 \vee A_{10} \vee A_{11} \vee \neg A_1$ ✓
- ...



⇒ conflict set: $\{\neg A_9, \neg A_{10}, \neg A_{11}, A_{12}, A_{13}\}$.

⇒ learn $C_{11} := A_9 \vee A_{10} \vee A_{11} \vee \neg A_{12} \vee \neg A_{13}$

The original backjumping strategy – example (13)

$$c_1 : \neg A_1 \vee A_2$$

$$c_2 : \neg A_1 \vee A_3 \vee A_9$$

$$c_3 : \neg A_2 \vee \neg A_3 \vee A_4$$

$$c_4 : \neg A_4 \vee A_5 \vee A_{10}$$

$$c_5 : \neg A_4 \vee A_6 \vee A_{11}$$

$$c_6 : \neg A_5 \vee \neg A_6$$

$$c_7 : A_1 \vee A_7 \vee \neg A_{12}$$

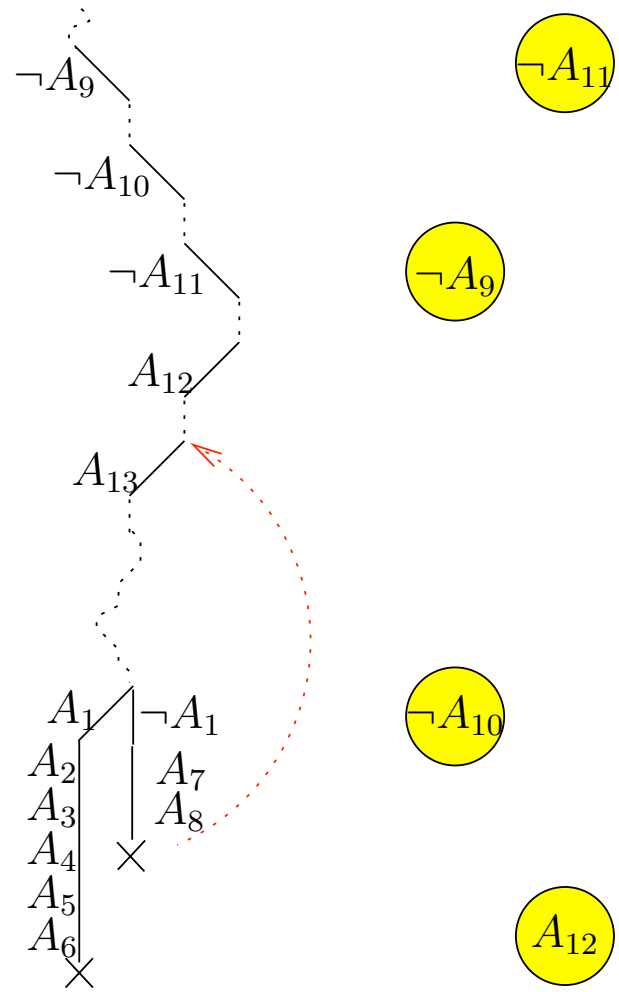
$$c_8 : A_1 \vee A_8$$

$$c_9 : \neg A_7 \vee \neg A_8 \vee \neg A_{13}$$

$$c_{10} : A_9 \vee A_{10} \vee A_{11} \vee \neg A_1$$

$$c_{11} : A_9 \vee A_{10} \vee A_{11} \vee \neg A_{12} \vee \neg A_{13}$$

...



⇒ backtrack to A_{13} ⇒ Lots of search saved!!!!!!!!!!!!

The original backjumping strategy – example (14)

$c_1 : \neg A_1 \vee A_2$

$c_2 : \neg A_1 \vee A_3 \vee A_9$

$c_3 : \neg A_2 \vee \neg A_3 \vee A_4$

$c_4 : \neg A_4 \vee A_5 \vee A_{10}$

$c_5 : \neg A_4 \vee A_6 \vee A_{11}$

$c_6 : \neg A_5 \vee \neg A_6$

$c_7 : A_1 \vee A_7 \vee \neg A_{12}$

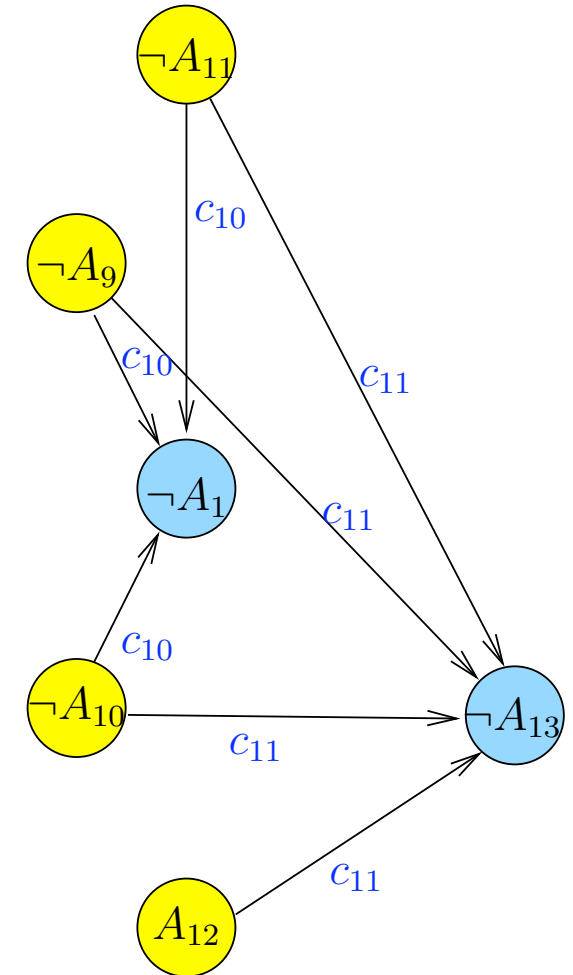
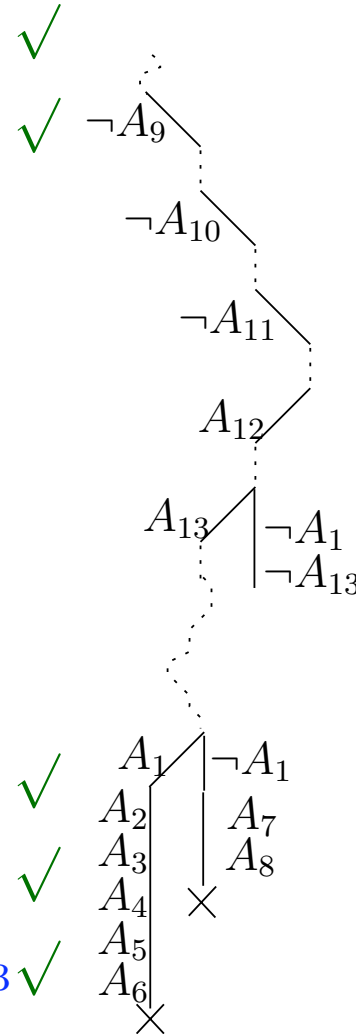
$c_8 : A_1 \vee A_8$

$c_9 : \neg A_7 \vee \neg A_8 \vee \neg A_{13}$

$c_{10} : A_9 \vee A_{10} \vee A_{11} \vee \neg A_1$

$c_{11} : A_9 \vee A_{10} \vee A_{11} \vee \neg A_{12} \vee \neg A_{13}$

...



⇒ backtrack to A_{13} , set A_{13} and A_1 to \perp, \dots

Learning [4, 46]

Idea: When a **conflict set** C is revealed, then $\neg C$ added to φ
 \implies DPLL will no more generate an assignment containing C : when
 $|C| - 1$ literals in C are assigned, the other is set \perp

Drastic pruning of the search!!!

Learning – example (cont.)

$$c_1 : \neg A_1 \vee A_2$$

$$c_2 : \neg A_1 \vee A_3 \vee A_9$$

$$c_3 : \neg A_2 \vee \neg A_3 \vee A_4$$

$$c_4 : \neg A_4 \vee A_5 \vee A_{10}$$

$$c_5 : \neg A_4 \vee A_6 \vee A_{11}$$

$$c_6 : \neg A_5 \vee \neg A_6$$

$$c_7 : A_1 \vee A_7 \vee \neg A_{12}$$

$$c_8 : A_1 \vee A_8$$

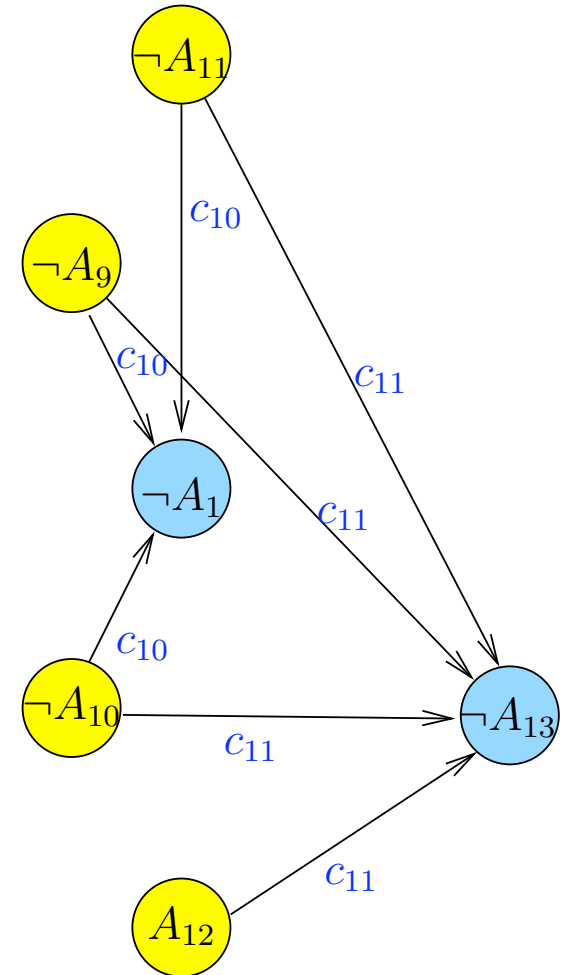
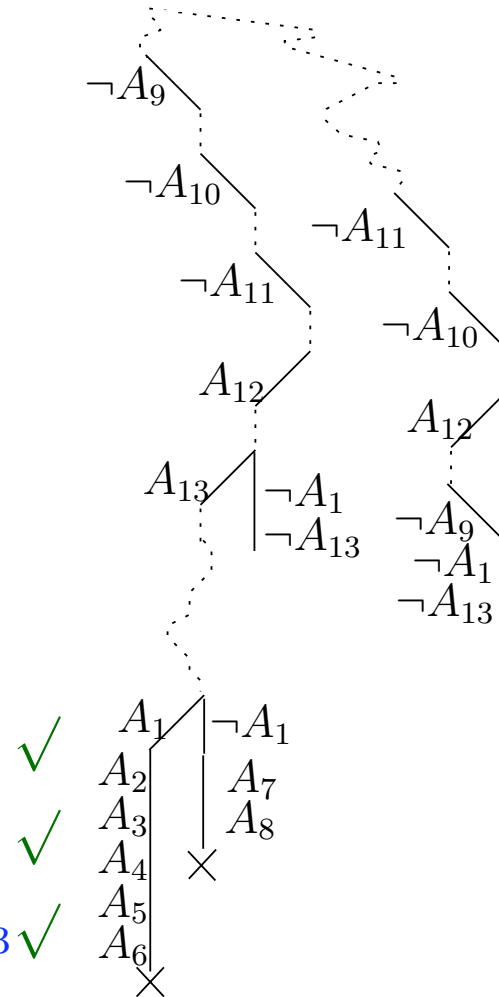
$$c_9 : \neg A_7 \vee \neg A_8 \vee \neg A_{13}$$

$$c_{10} : A_9 \vee A_{10} \vee A_{11} \vee \neg A_1$$

$$c_{11} : A_9 \vee A_{10} \vee A_{11} \vee \neg A_{12} \vee \neg A_{13}$$

...

⇒ Unit: $\{\neg A_1, \neg A_{13}\}$



State-of-the-art backjumping and learning [54]

- ▷ **Idea:** when a branch μ fails,
1. **conflict analysis:** find the conflict set $\eta \subseteq \mu$ by generating the conflict clause $C \stackrel{\text{def}}{=} \neg\eta$ via resolution from the falsified clause, according to the 1stUIP strategy
 2. **learning:** add the conflict clause C to the clause set
 3. **backjumping:** backtrack to the highest branching point s.t. the stack contains all-but-one literals in η , and then unit-propagate the unassigned literal on C

State-of-the-art backjumping and learning: intuitions

- ▷ **Backjumping:** allows for climbing up to many decision levels in the stack
⇒ may avoid lots of redundant search
 - intuition: “go back to the oldest decision where you’d have done something different if only you had known C ”
- ▷ **Learning:** in future branches, when all-but-one literals in η are assigned, the remaining literal is assigned to false by unit-propagation:
⇒ avoid finding the same conflict again
 - intuition: “when you’re about to repeat the mistake, do the opposite of the last step”

State-of-the-art in backjumping & learning [54]

- ▷ A node l in an implication graph is an **unique implication point** (UIP) for the last decision level iff any path from the last decision node to both the conflict nodes passes through l .
 - the most recent decision node is an UIP (**last UIP**)
 - all other UIP's have been assigned after the most recent decision

State-of-the-art in backjumping & learning [54]

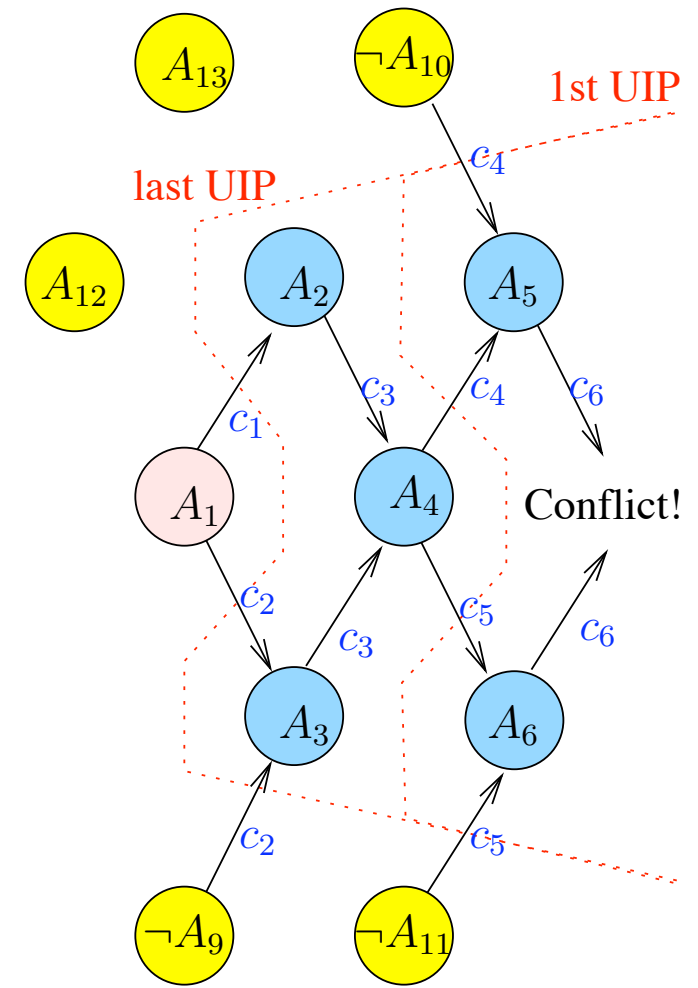
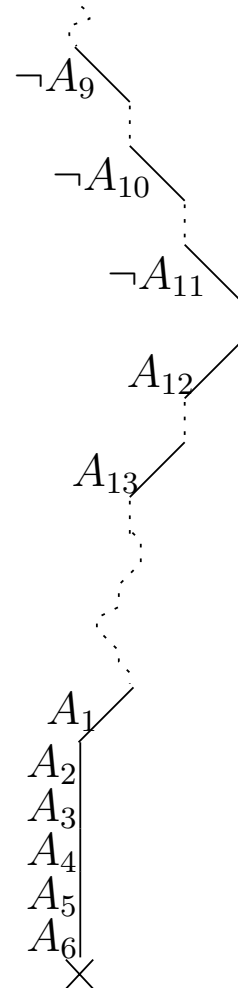
First Unique Implication Point (1st UIP) strategy:

- ▷ **1st UIP strategy**: adopt the partition involving the 1st UIP for the last decision level.
- ▷ corresponds to consider the first clause encountered containing one literal of the current level (1st UIP).

$$\begin{array}{c}
 \frac{\neg A_4 \vee A_5 \vee A_{10}}{\underbrace{\neg A_4}_{1st\ UIP} \vee A_{10} \vee A_{11}} \quad \frac{\neg A_4 \vee A_6 \vee A_{11} \quad \overbrace{\neg A_5 \vee \neg A_6}^{Conflicting\ cl.}}{\neg A_4 \vee \neg A_5 \vee A_{11}} \quad (A_5) \quad (A_6)
 \end{array}$$

1st UIP strategy – example (7)

- $c_1 : \neg A_1 \vee A_2$ ✓
- $c_2 : \neg A_1 \vee A_3 \vee A_9$ ✓
- $c_3 : \neg A_2 \vee \neg A_3 \vee A_4$ ✓
- $c_4 : \neg A_4 \vee A_5 \vee A_{10}$ ✓
- $c_5 : \neg A_4 \vee A_6 \vee A_{11}$ ✓
- $c_6 : \neg A_5 \vee \neg A_6$ ✗
- $c_7 : A_1 \vee A_7 \vee \neg A_{12}$ ✓
- $c_8 : A_1 \vee A_8$ ✓
- $c_9 : \neg A_7 \vee \neg A_8 \vee \neg A_{13}$
- ...



⇒ Conflict set: $\{\neg A_{10}, \neg A_{11}, A_4\}$, learn $c_{10} := A_{10} \vee A_{11} \vee \neg A_4$

1st UIP strategy and backjumping [54]

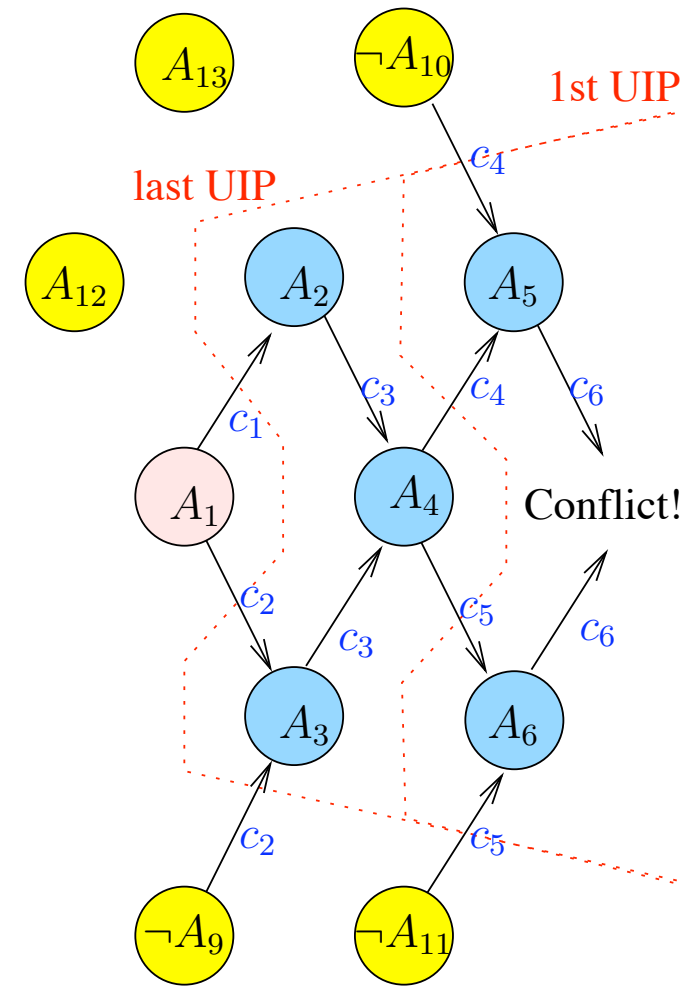
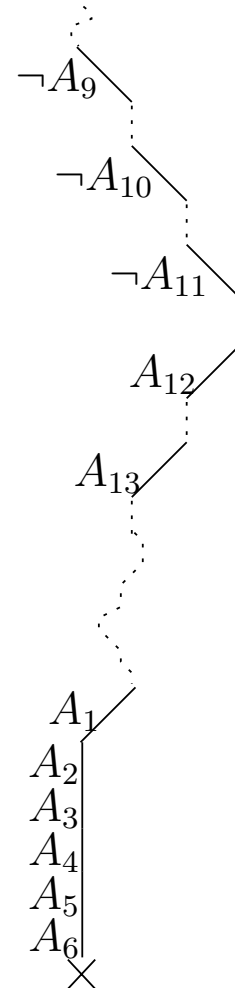
- ▷ The added conflict clause states the reason for the conflict
- ▷ The procedure backtracks to the most recent decision level of the variables in the conflict clause which are not the UIP.
- ▷ then the conflict clause forces the negation of the UIP by unit propagation.

E.g.: $c_{10} := A_{10} \vee A_{11} \vee \neg A_4$

\implies backtrack to A_{11} , then assign $\neg A_4$

1st UIP strategy – example (7)

- $c_1 : \neg A_1 \vee A_2$ ✓
- $c_2 : \neg A_1 \vee A_3 \vee A_9$ ✓
- $c_3 : \neg A_2 \vee \neg A_3 \vee A_4$ ✓
- $c_4 : \neg A_4 \vee A_5 \vee A_{10}$ ✓
- $c_5 : \neg A_4 \vee A_6 \vee A_{11}$ ✓
- $c_6 : \neg A_5 \vee \neg A_6$ ✗
- $c_7 : A_1 \vee A_7 \vee \neg A_{12}$ ✓
- $c_8 : A_1 \vee A_8$ ✓
- $c_9 : \neg A_7 \vee \neg A_8 \vee \neg A_{13}$
- ...



⇒ Conflict set: $\{\neg A_{10}, \neg A_{11}, A_4\}$, learn $c_{10} := A_{10} \vee A_{11} \vee \neg A_4$

1st UIP strategy – example (8)

$$c_1 : \neg A_1 \vee A_2$$

$$c_2 : \neg A_1 \vee A_3 \vee A_9$$

$$c_3 : \neg A_2 \vee \neg A_3 \vee A_4$$

$$c_4 : \neg A_4 \vee A_5 \vee A_{10}$$

$$c_5 : \neg A_4 \vee A_6 \vee A_{11}$$

$$c_6 : \neg A_5 \vee \neg A_6$$

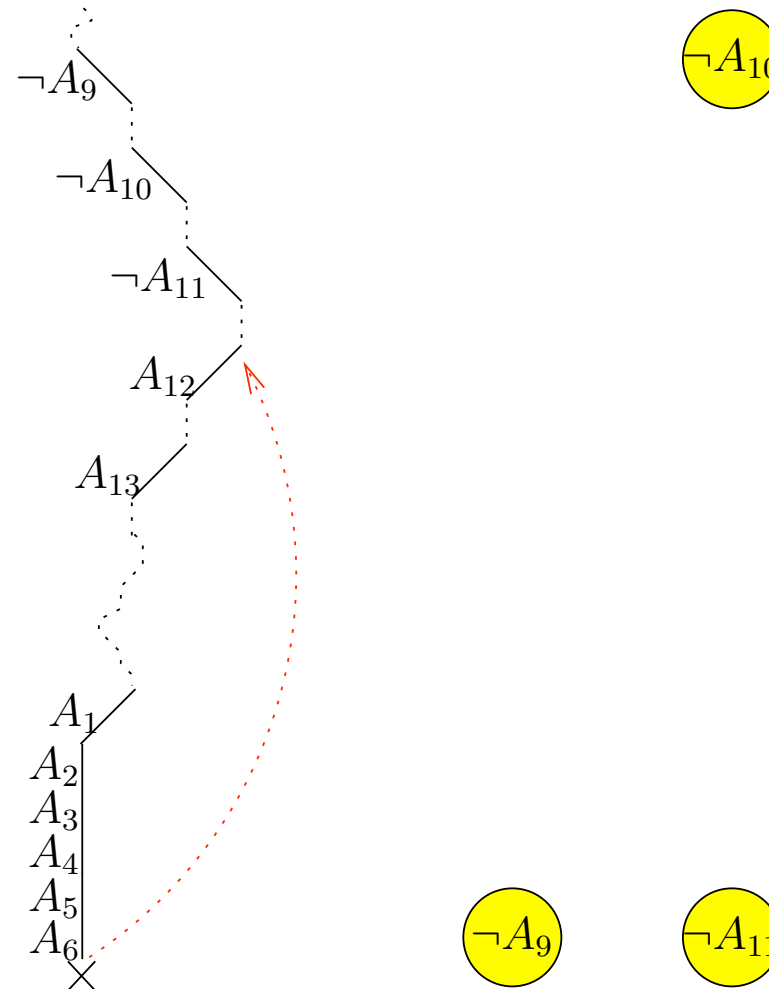
$$c_7 : A_1 \vee A_7 \vee \neg A_{12}$$

$$c_8 : A_1 \vee A_8$$

$$c_9 : \neg A_7 \vee \neg A_8 \vee \neg A_{13}$$

$$c_{10} : A_{10} \vee A_{11} \vee \neg A_4$$

...



\implies backtrack up to $A_{11} \implies \{\dots, \neg A_9, \neg A_{10}, \neg A_{11}\}$

1st UIP strategy – example (9)

$$c_1 : \neg A_1 \vee A_2$$

$$c_2 : \neg A_1 \vee A_3 \vee A_9$$

$$c_3 : \neg A_2 \vee \neg A_3 \vee A_4$$

$$c_4 : \neg A_4 \vee A_5 \vee A_{10} \quad \checkmark$$

$$c_5 : \neg A_4 \vee A_6 \vee A_{11} \quad \checkmark$$

$$c_6 : \neg A_5 \vee \neg A_6$$

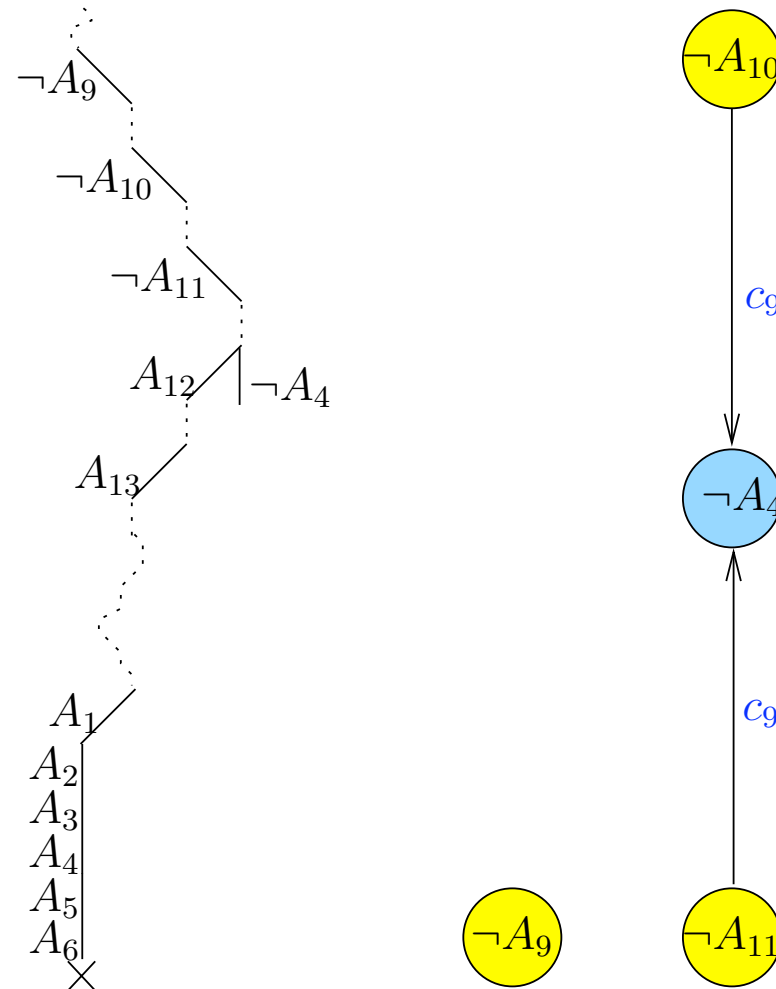
$$c_7 : A_1 \vee A_7 \vee \neg A_{12}$$

$$c_8 : A_1 \vee A_8$$

$$c_9 : \neg A_7 \vee \neg A_8 \vee \neg A_{13}$$

$$c_{10} : A_{10} \vee A_{11} \vee \neg A_4 \quad \checkmark$$

...



\implies unit propagate $\neg A_4 \implies \{\dots, \neg A_9, \neg A_{10}, \neg A_{11}, A_4\} \dots$

1st UIP strategy and backjumping – intuition

- ▷ An UIP is a **single** reason implying the conflict at the current level
- ▷ substituting the 1st UIP for the last UIP
 - does not enlarge the conflict
 - may require involving less decision literals from other levels
- ▷ by backtracking to the most recent decision level of the variables in the conflict clause which are not the UIP:
 - jump higher
 - allows for assigning (the negation of) the UIP as high as possible in the search tree.

Remark: the “quality” of conflict sets

- ▷ Different ideas of “good” conflict set
 - Backjumping: if causes the highest backjump (“local” role)
 - Learning: if causes the maximum pruning (“global” role)
- ▷ Many different strategies implemented (see, e.g., [4, 46, 54])

Drawbacks of Learning

- ▷ Prunes drastically the search.
- ▷ **Problem:** may cause a blowup in space
 - ⇒ techniques to drop learned clauses when necessary
 - according to their size
 - according to their **activity**.

Restarts [24]

(according to some strategy) restart DPLL

- ▷ abandon the current search tree and reconstruct a new one
- ▷ The clauses learned prior to the restart are still there after the restart and can help pruning the search space
- ▷ may significantly reduce the overall search space

What is missing?

...Many things:

1. Data structures for effective BCP (binary clauses, two literal watching, BCP ordering)
2. Forgetting policies
3. Effective use of L1 and L2 cache
4. Parallel and manycore SAT solvers
5. Phase saving
6. ...

Content

- ✓ Basics on SAT
- ✓ NNF, CNF and conversions
- ✓ Basic SAT techniques
- ✓ Modern SAT Solvers
- ⇒ Advanced Functionalities: proofs, unsat cores, interpolants

Advanced functionalities

Advanced SAT functionalities (very important in formal verification):

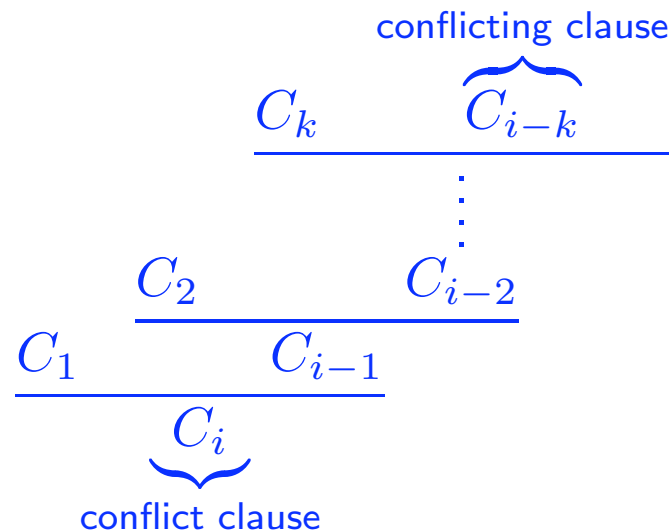
- ▷ Building **proofs of unsatisfiability**
- ▷ Extracting **unsatisfiable Cores**

Building Proofs of Unsatisfiability

- ▷ When φ is unsat, it is very important to build a (resolution) proof of unsatisfiability:
 - to verify the result of the solver
 - to understand a “reason” for unsatisfiability
 - to build unsatisfiable cores and interpolants
- ▷ can be built by keeping track of the resolution steps performed when constructing the conflict clauses.

Building Proofs of Unsatisfiability

- ▷ recall: each conflict clause C_i learned is computed from the conflicting clause C_{i-k} by backward resolving with the antecedent clause of one literal



- ▷ C_1, \dots, C_k , and C_{i-k} can be original or learned clauses
- ▷ each resolution (sub)proof can be easily tracked:

$i \quad i-k \rightarrow i-k-1$

...

$2 \quad i-2 \rightarrow i-1$

$1 \quad i-1 \rightarrow i$

Building Proofs of Unsatisfiability

▷ ... in particular, if φ is unsatisfiable, the last step produces “false” as conflict clause:

$$\begin{array}{c}
 \text{conflicting clause} \\
 \overline{C_k \quad C_{i-k}} \\
 \vdots \\
 \overline{C_2 \quad C_{i-2}} \\
 \overline{C_1 \quad C_{i-1}} \\
 \perp
 \end{array}$$

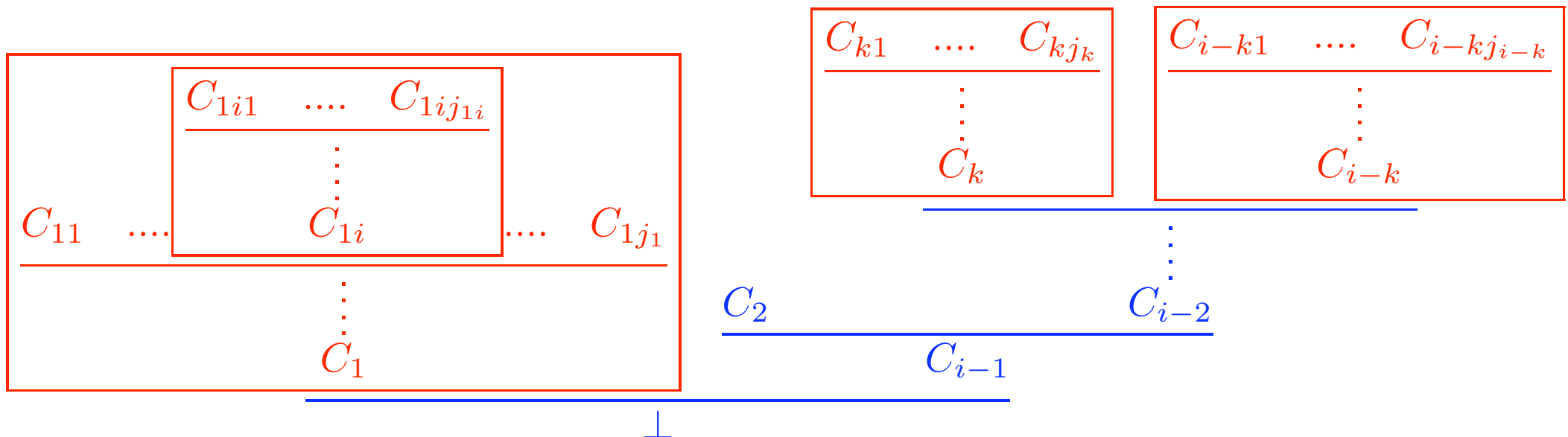
▷ note: $C_1 = l$, $C_{i-1} = \neg l$ for some literal l

▷ C_1, \dots, C_k , and C_{i-k} can be original or learned clauses...

Building Proofs of Unsatisfiability

Starting from the previous proof of unsatisfiability, repeat recursively:

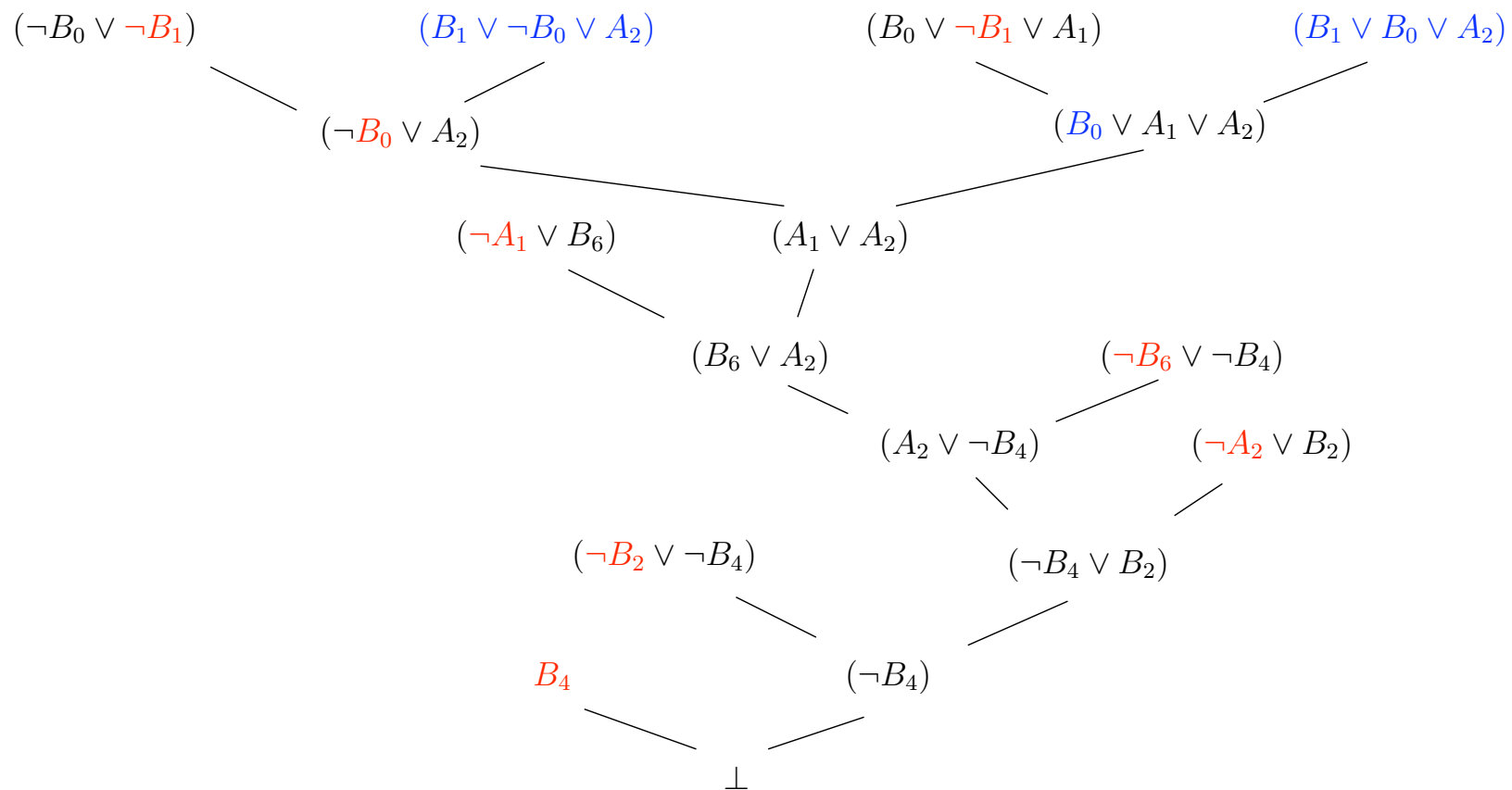
- ▷ for every **learned** leaf clause C_i , substitute C_i with the resolution proof generating it until all leaf clauses are original clauses



\implies we obtain a resolution proof of unsatisfiability for (a subset of) the clauses in φ

Building Proofs of Unsatisfiability: example

$$(B_0 \vee \neg B_1 \vee A_1) \wedge (B_0 \vee B_1 \vee A_2) \wedge (\neg B_0 \vee B_1 \vee A_2) \wedge (\neg B_0 \vee \neg B_1) \wedge (\neg B_2 \vee \neg B_4) \wedge (\neg A_2 \vee B_2) \wedge (\neg A_1 \vee B_3) \wedge B_4 \wedge (A_2 \vee B_5) \wedge (\neg B_6 \vee \neg B_4) \wedge (B_6 \vee \neg A_1) \wedge B_7$$



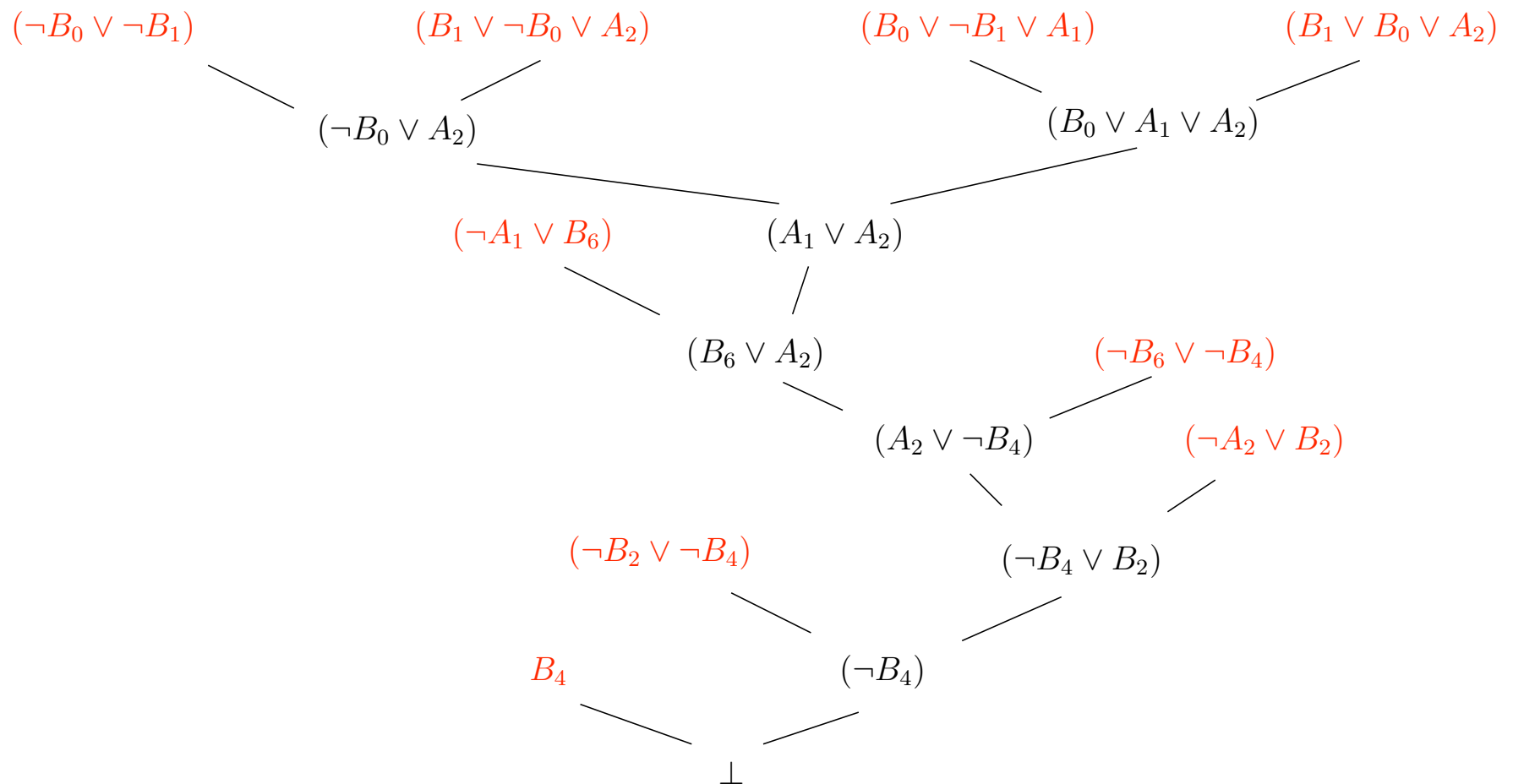
Extraction of unsatisfiable cores

- ▷ **Problem:** given a unsatisfiable set of clauses, extract from it a (possibly small/minimal/minimum) unsatisfiable subset
- ▷ Lots of literature on the topic [56, 30, 32, 38, 53, 25, 19, 6]
- ▷ We recognize two main approaches:
 - **Proof-based** approach [56]: byproduct of finding a resolution proof
 - **Assumption-based** approach [30]: use extra variables labeling clauses
- ▷ many optimizations for further reducing the size of the core:
 - repeat the process up to fixpoint
 - remove clauses one-by one, until satisfiability is obtained
 - combinations of the two processed above
 - ...

The proof-based approach to unsat-core extraction

Unsat core: the set of leaf clauses of a resolution proof

$$(B_0 \vee \neg B_1 \vee A_1) \wedge (B_0 \vee B_1 \vee A_2) \wedge (\neg B_0 \vee B_1 \vee A_2) \wedge (\neg B_0 \vee \neg B_1) \wedge (\neg B_2 \vee \neg B_4) \wedge (\neg A_2 \vee B_2) \wedge (\neg A_1 \vee B_3) \wedge B_4 \wedge (A_2 \vee B_5) \wedge (\neg B_6 \vee \neg B_4) \wedge (B_6 \vee \neg A_1) \wedge B_7$$



The assumption-based approach to unsat-core extraction

Based on the following process:

1. each clause C_i is substituted by $S_i \rightarrow C_i$, s.t. S_i fresh “selector” variable
2. before starting the search each S_i is forced to true.
3. final conflict clause at dec. level 0: $\bigvee_j \neg S_j$
 $\implies \{C_j\}_j$ is the unsat core

The assumption-based approach to unsat-core extraction

$$\begin{aligned}
 & (B_0 \vee \neg B_1 \vee A_1) \wedge (B_0 \vee B_1 \vee A_2) \wedge (\neg B_0 \vee B_1 \vee A_2) \wedge \\
 & (\neg B_0 \vee \neg B_1) \wedge (\neg B_2 \vee \neg B_4) \wedge (\neg A_2 \vee B_2) \wedge (\neg A_1 \vee B_3) \wedge \\
 & B_4 \wedge (A_2 \vee B_5) \wedge (\neg B_6 \vee \neg B_4) \wedge (B_6 \vee \neg A_1) \wedge B_7
 \end{aligned}$$

add selector variables:

$$\begin{aligned}
 & S_1 \rightarrow (B_0 \vee \neg B_1 \vee A_1) \wedge S_2 \rightarrow (B_0 \vee B_1 \vee A_2) \wedge S_3 \rightarrow (\neg B_0 \vee B_1 \vee A_2) \wedge \\
 & S_4 \rightarrow (\neg B_0 \vee \neg B_1) \wedge S_5 \rightarrow (\neg B_2 \vee \neg B_4) \wedge S_6 \rightarrow (\neg A_2 \vee B_2) \wedge (S_7 \rightarrow \neg A_1 \vee B_3) \wedge \\
 & S_8 \rightarrow B_4 \wedge S_9 \rightarrow (A_2 \vee B_5) \wedge S_{10} \rightarrow (\neg B_6 \vee \neg B_4) \wedge S_{11} \rightarrow (B_6 \vee \neg A_1) \wedge S_{12} \rightarrow B_7
 \end{aligned}$$

The conflict analysis returns:

$$\neg S_1 \vee \neg S_2 \vee \neg S_3 \vee \neg S_4 \vee \neg S_5 \vee \neg S_6 \vee \neg S_8 \vee \neg S_{10} \vee \neg S_{11},$$

corresponding to the unsat core:

$$\begin{aligned}
 & (B_0 \vee \neg B_1 \vee A_1) \wedge (B_0 \vee B_1 \vee A_2) \wedge (\neg B_0 \vee B_1 \vee A_2) \wedge \\
 & (\neg B_0 \vee \neg B_1) \wedge (\neg B_2 \vee \neg B_4) \wedge (\neg A_2 \vee B_2) \wedge \\
 & B_4 \wedge (\neg B_6 \vee \neg B_4) \wedge (B_6 \vee \neg A_1)
 \end{aligned}$$

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